

8 7 6 5 4 3 2 1
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F HSF Property: ROHS or Halogen-Free(5L3?)

CALVIN DIS

DB BUILD

2011.05.23

23 MAY 2011		
DATE	CHANGE NO.	REV

	EE	DATE	POWER	DATE	INVENTEC			
DRAWER					TITLE MODEL PROJECT FUNCTION PROJECT NAME			
DESIGN								
CHECK								
RESPONSIBLE								
SIZE				VER:	SIZE	CODE	DOC NUMBER	REV
FILE NAME					5	05	1310KXKX-0-0	2011
PIN	Class Change				SHEET 1 of 22			

TABLE OF CONTENTS

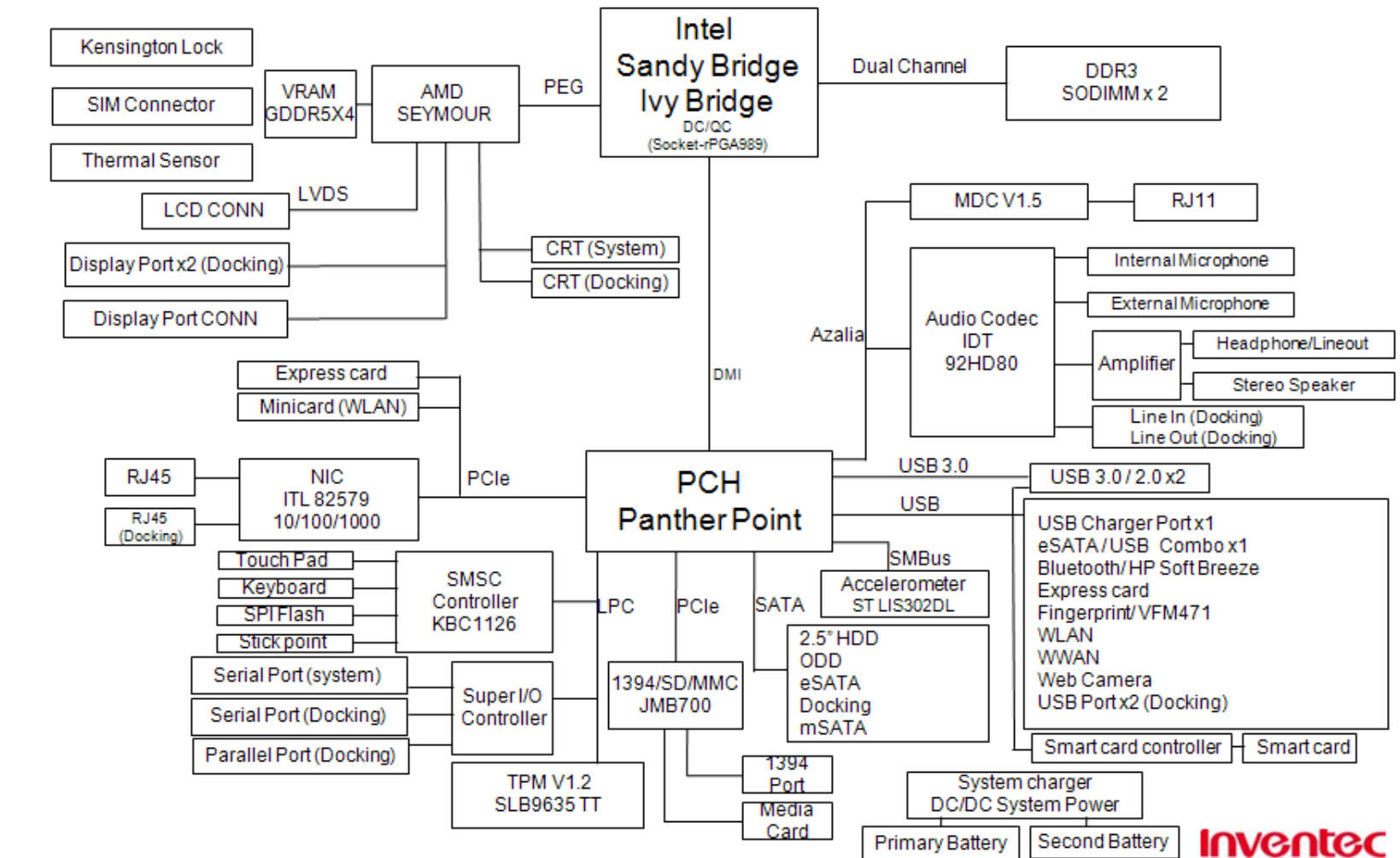
1. PROJECT NAME	28. IVY BRIDGE_6 (GND,RESERVED)	54. WWAN & WLAN & SIM
2. TABLE OF CONTENTS	29. DDR3_SO-DIMM0	55. DOCKING CONN
3. BLOCK DIAGRAM	30. DDR3_SO-DIMM1	56. POWER & WLAN_LED
4. BATTERY CHARGER	31. PANTER POINT_1 (HDA,JTAG,SPI,SATA)	57. SCREW
5. SELECTOR & BATTERY CONN	32. PANTER POINT_2 (PCI-E,SMBUS,CLK)	58. WIRELESS / QUICK WEB / MUTE LED
6. OCP	33. PANTER POINT_3 (DMI,FDI,GPIO)	59. SMART CARD
7. SYSTEM POWER (P3V3 / P5V0)	34. PANTER POINT_4 (LVDS,DDI)	60. EXPRESS CARD CONN
8. SYSTEM POWER (P3V3 / P5V0_PORT)	35. PANTER POINT_5 (PCI,USB,NVRAM)	61. EXPRESS CARD/POINT STICH/TOUCH PAD
9. SYSTEM POWER (P1V5)	36. PANTER POINT_6 (GPIO,VSS_NCFT,RSVD)	62. AUDIO
10. SYSTEM POWER (P1V05_LAN_M)	37. PANTER POINT_7 (POWER)	63. EXT. MIC / MIC / HP
11. SYSTEM POWER (P1V0_VCCP)	38. PANTER POINT_8 (POWER)	64. CARD READER CTRL/1394 CONN/SD CONN
12. SYSTEM POWER (P1V8S)	39. PANTER POINT_9 (GND)	65. LID SWITCH
13. CPU POWER(PVSA)	40. VGA SWITCH / CRT CONN	66. EMI & RF SOLUTION
14. PVCORE_1(PV_CORE / PV_AXG)	41. DISPLAY PORT CONN	67. GPU-1
15. PVCORE_2(PV_CORE / PV_AXG)	42. LCM & WEBCAM	68. GPU-2
16. PVCORE_DGPU	43. BLANK	69. GPU-3
17. P1V35_DGPU	44. SATA HDD & ODD CONN	70. GPU-4
18. HP_OCP	45. USB & ESATA COMBO / USB CHARGER	71. VRAM-1
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20. POWER(SEQUENCE)	47. BLUE TOOTH & FINGER PRINTER CONN	
21. FAN & THERMAL	48. ACCELEMETOR	
22. XDP CONN	49. KBC & SPI	
23. IVY BRIDGE_1 (CLK,MISC,JTAG)	50. KEYBOARD	
24. IVY BRIDGE_2 (POWER)	51. SUPER I/O	
25. IVY BRIDGE_3 (DMI,DP,PEG,FDI)	52. TPM & MDC	
26. IVY BRIDGE_4 (DDR3)	53. LAN (NIC,TRANSFORMER,RJ45)	
27. IVY BRIDGE_5 (GRAPHICS POWER)		

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TITLE			
MODEL PROJECT FUNCTION TABLE OF CONTENTS			
SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
SHEET 2 of 72			

CHANGE by First Chang DATE 23-MAY-2011

CALVIN Discrete Block Diagram



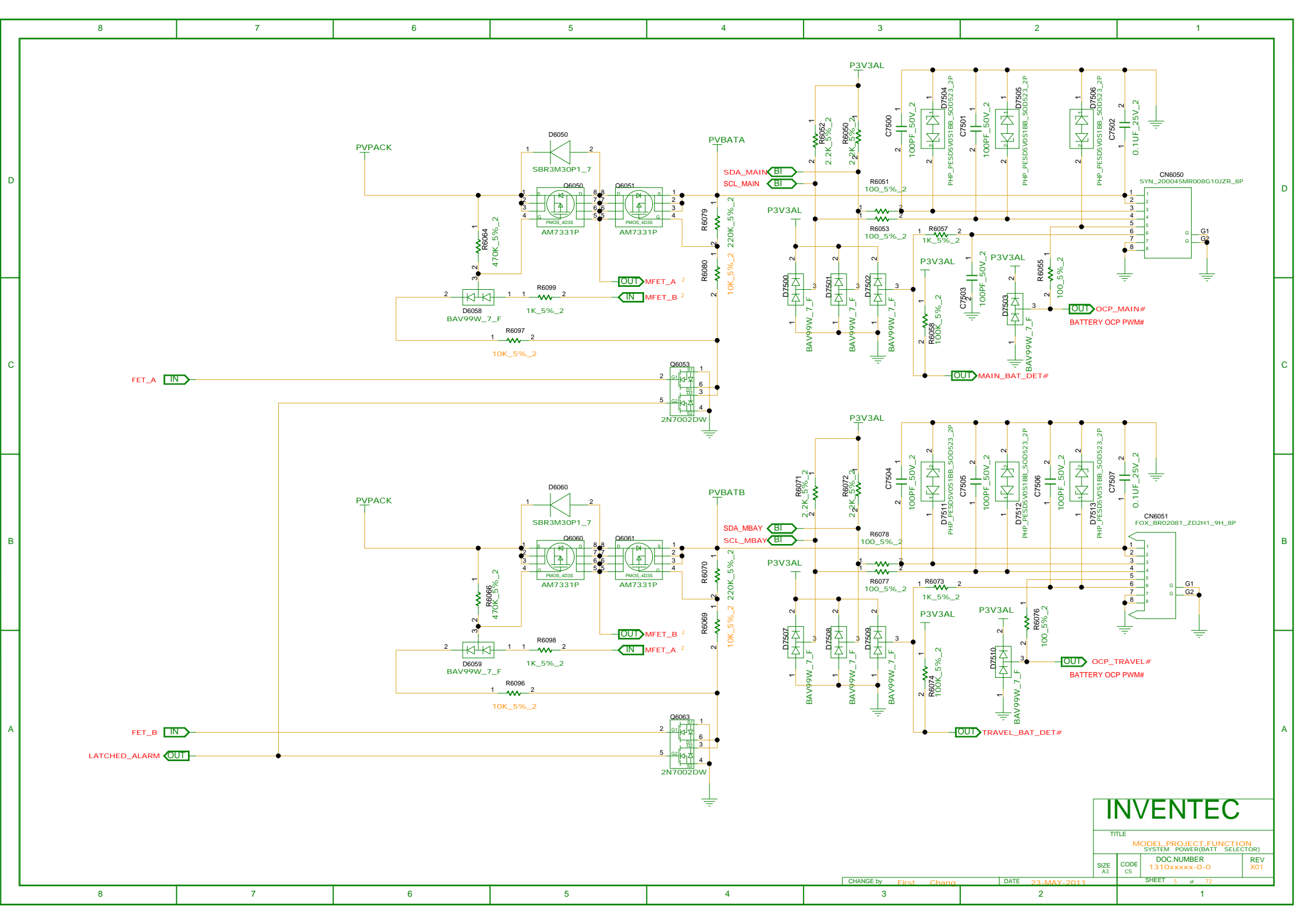
Inventec

INVENTEC

TITLE			
MODEL, PROJECT, FUNCTION			
BLOCK DIAGRAM			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

CHANGE by First Chang DATE 23-MAY-2011

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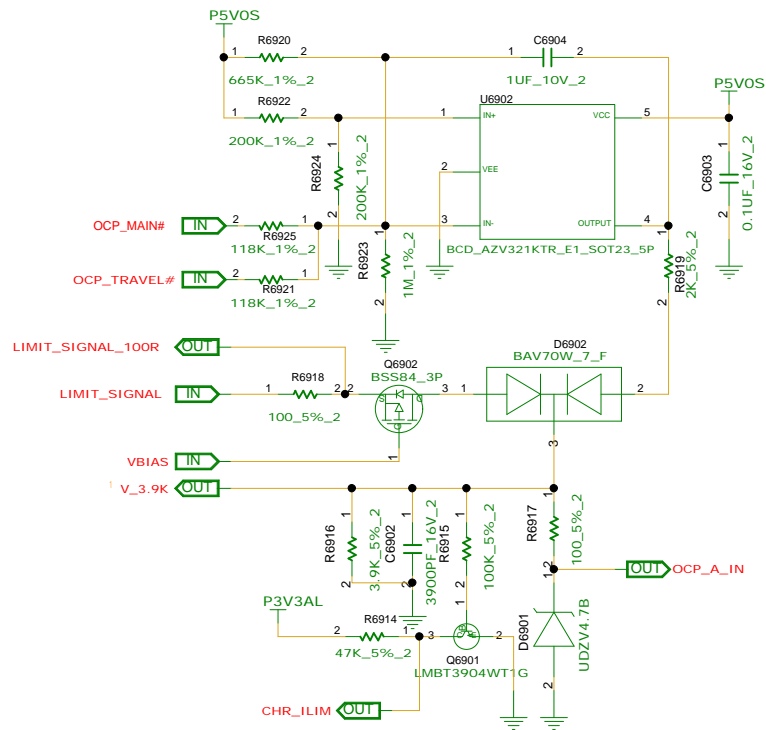
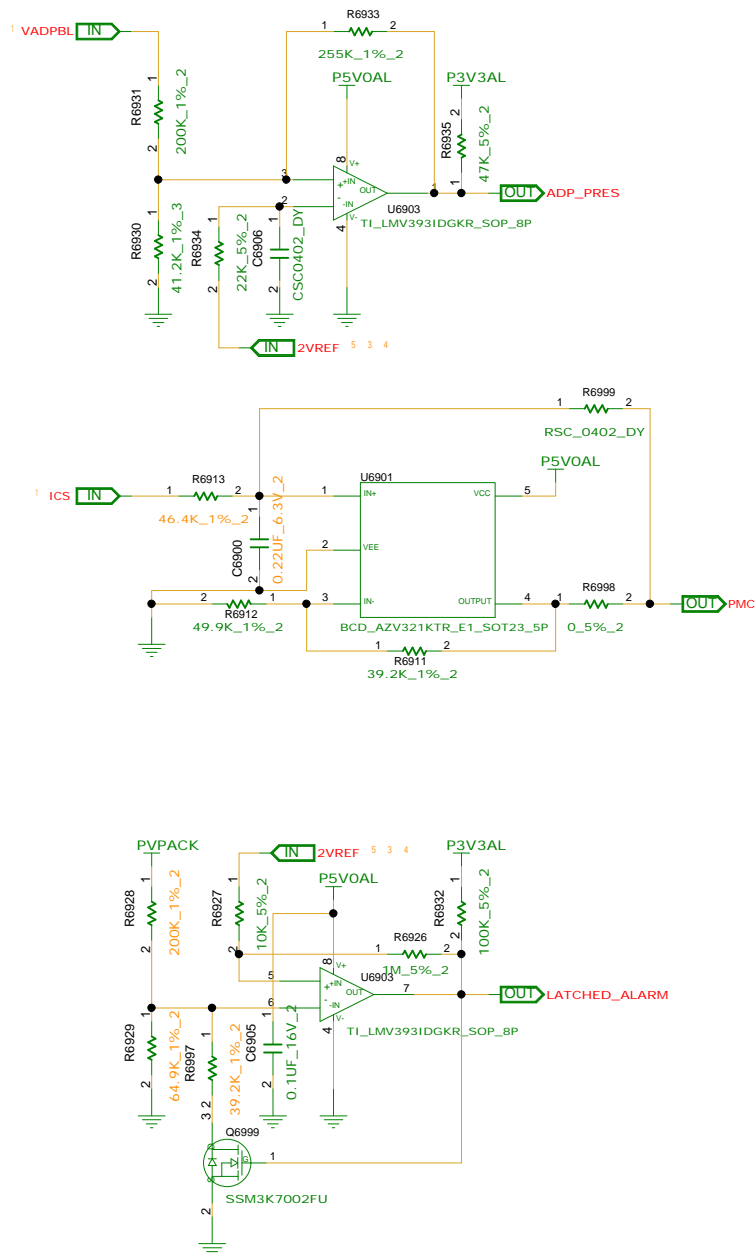


INVENTEC

TITLE			
MODEL PROJECT FUNCTION SYSTEM POWER(BATT_SELECTOR)			
DOC NUMBER 1310xxxxx-0-0			
SIZE A3	CODE CS	REV X01	

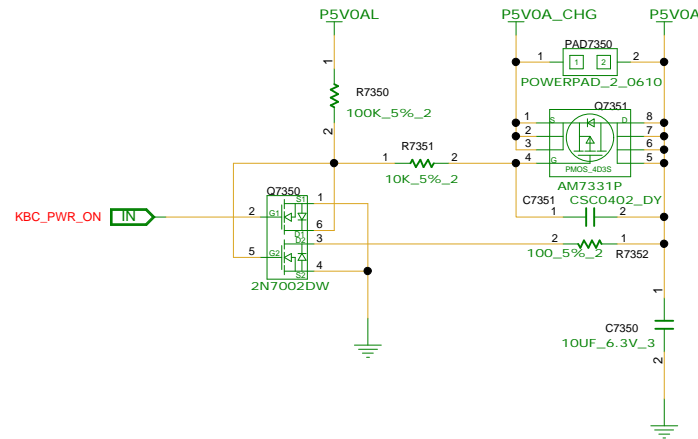
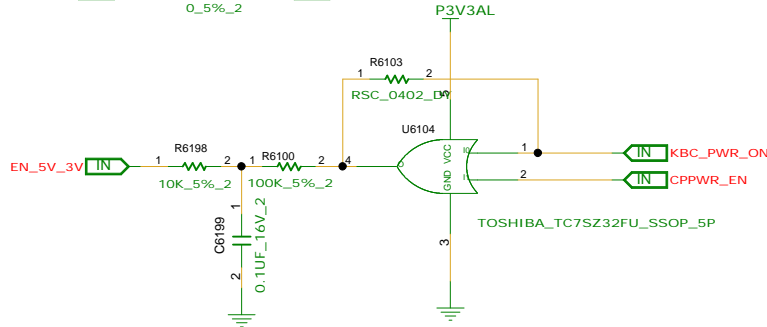
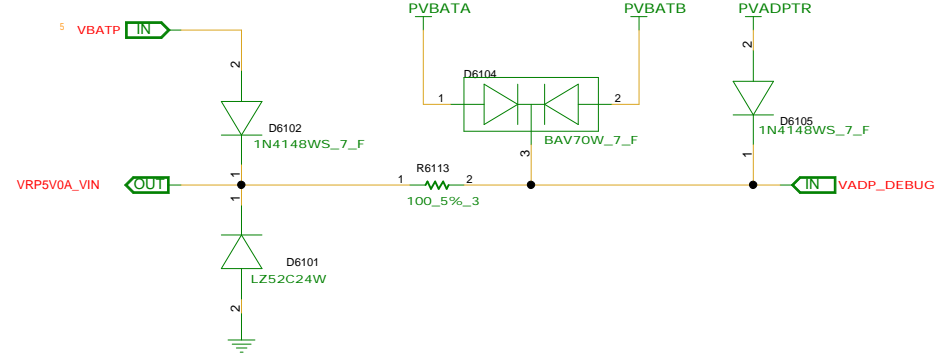
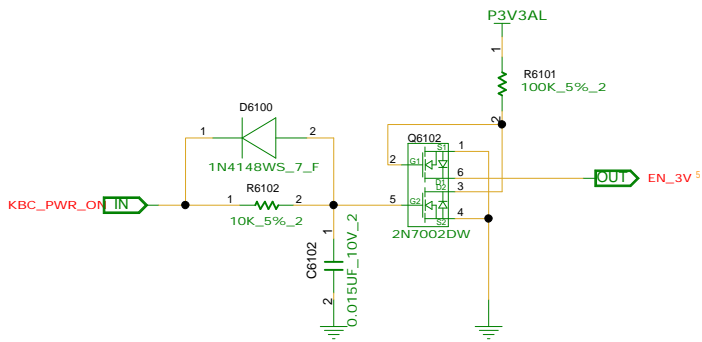
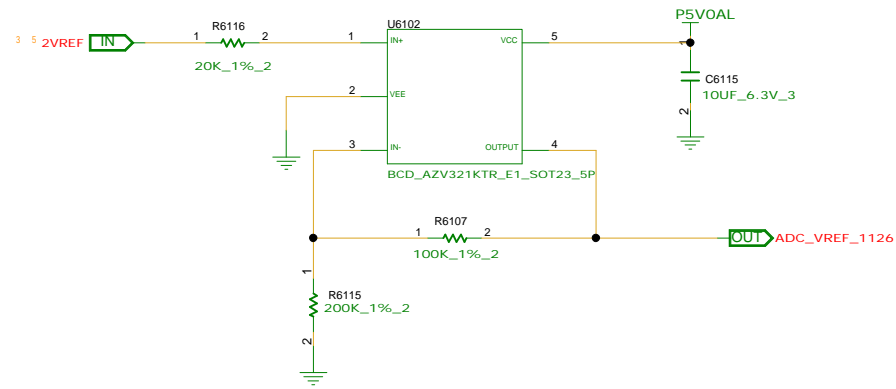
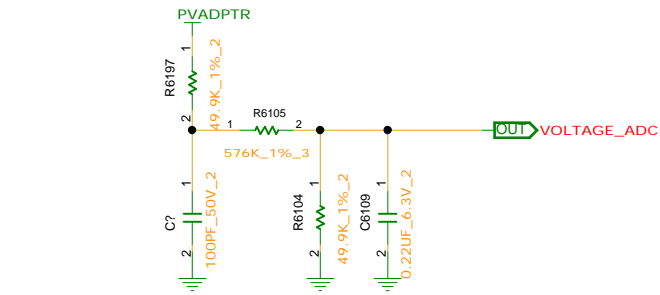
CHANGE by First Chang DATE 23-MAY-2011

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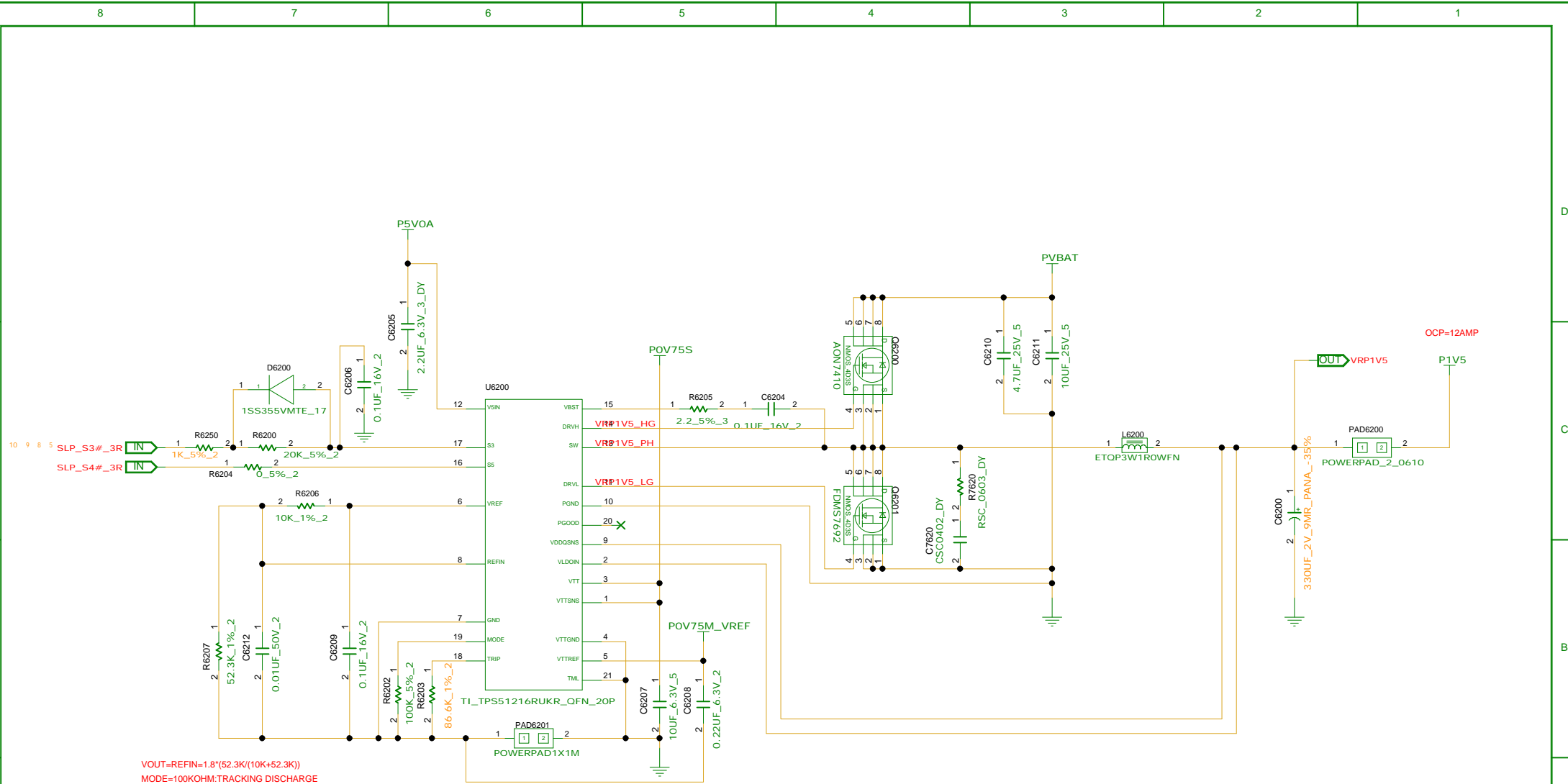


INVENTEC				
TITLE				
MODEL PROJECT FUNCTION				
SYSTEM POWER(OCP)				
SIZE	CODE	DOC NUMBER	REV	
A3	CS	1310xxxxx-0-0	X01	
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CHANGE by First Chang DATE 23-MAY-2011



INVENTEC			
TITLE			
MODEL PROJECT FUNCTION			
P3V3A&P5VOA_CHG PORT			
DOC NUMBER			
1310xxxxx-0-0			
REV			
X01			



VOUT=REFIN=1.8*(52.3K/(10K+52.3K))
MODE=100KOHM:TRACKING DISCHARGE

Table 1. S3/S5 Power State Control

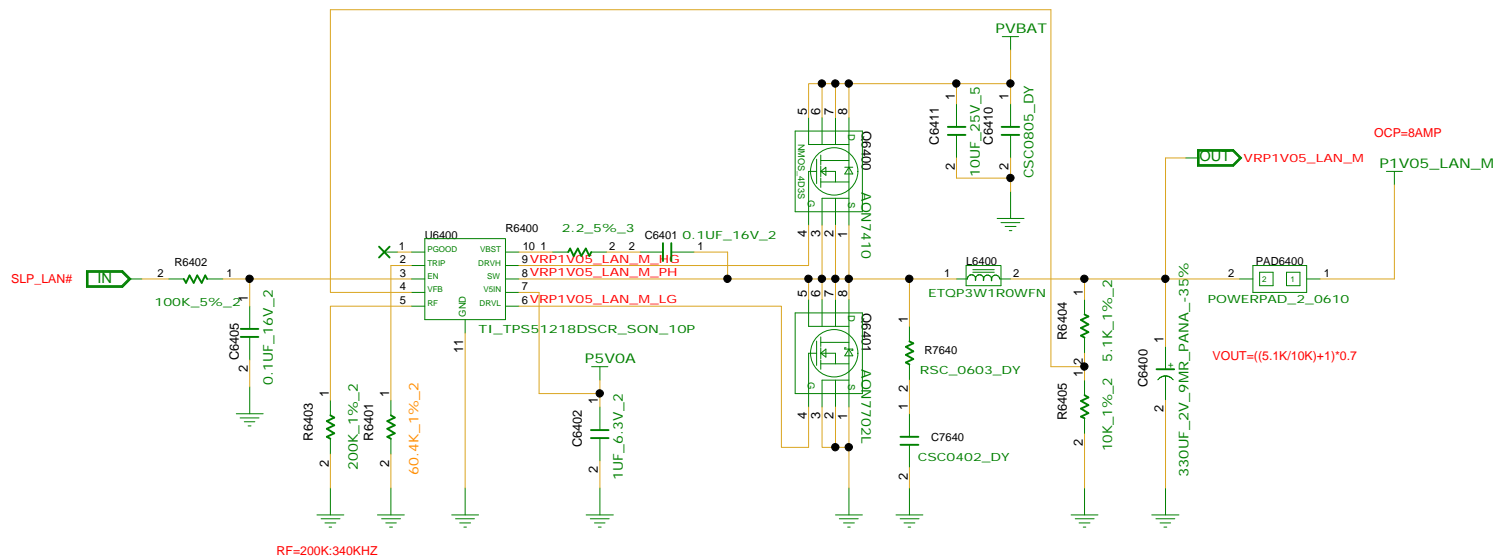
STATE	S3	S5	VREF	VDDQ	VTTREF	VTT
S0	HI	HI	ON	ON	ON	ON
S3	LO	HI	ON	ON	ON	OFF(High-Z)
S4/S5	LO	LO	OFF	OFF(Discharge)	OFF(Discharge)	OFF(Discharge)

INVENTEC

TITLEMODEL PROJECT FUNCTIONSYSTEM POWER(P1V5)

SIZE A3CODE CSDOC NUMBER1310xxxxx-0-0REV X01

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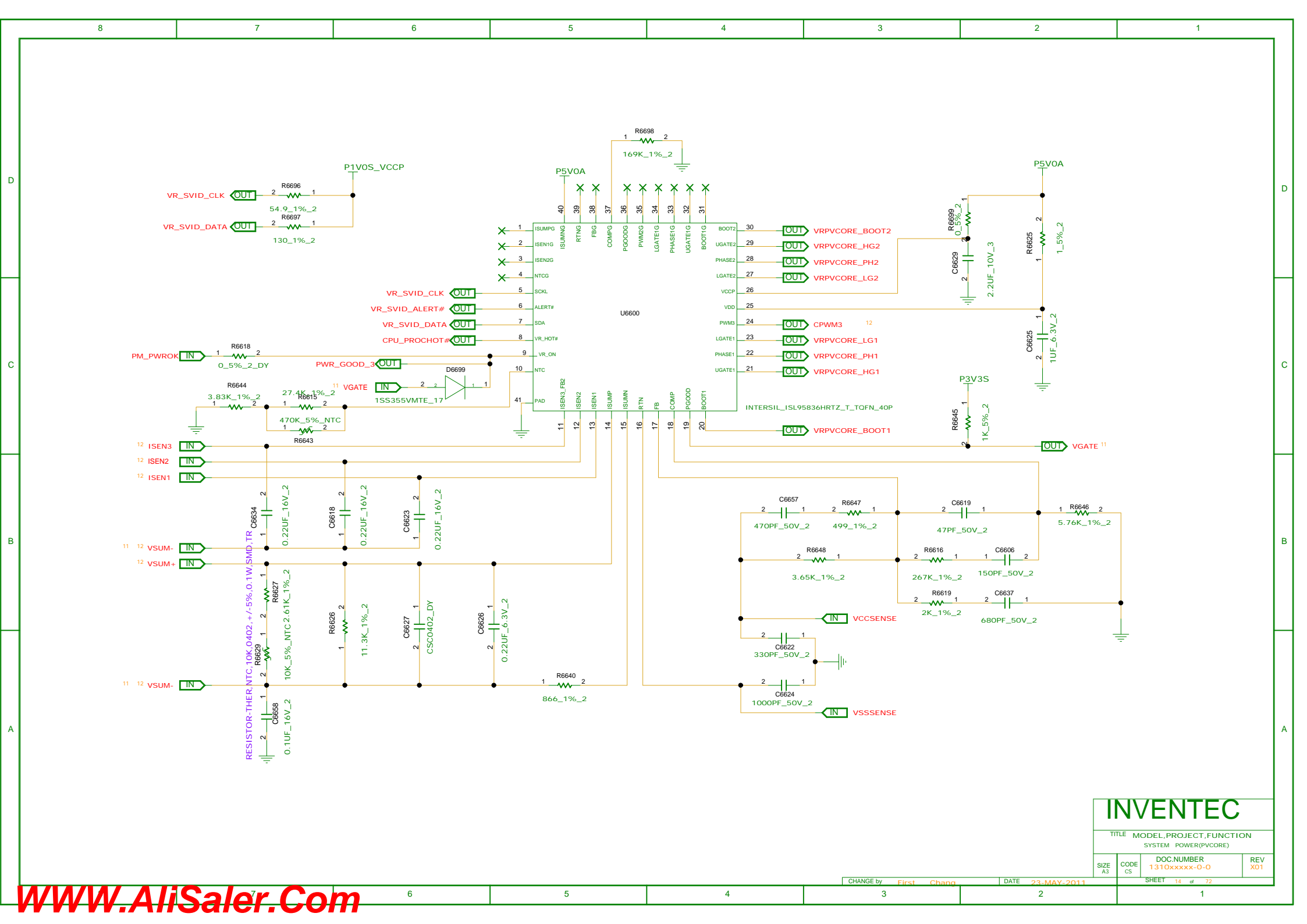
INVENTEC

TITLE MODEL,PROJECT,FUNCTION
SYSTEM POWER(P1V05_LAN_M)

SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
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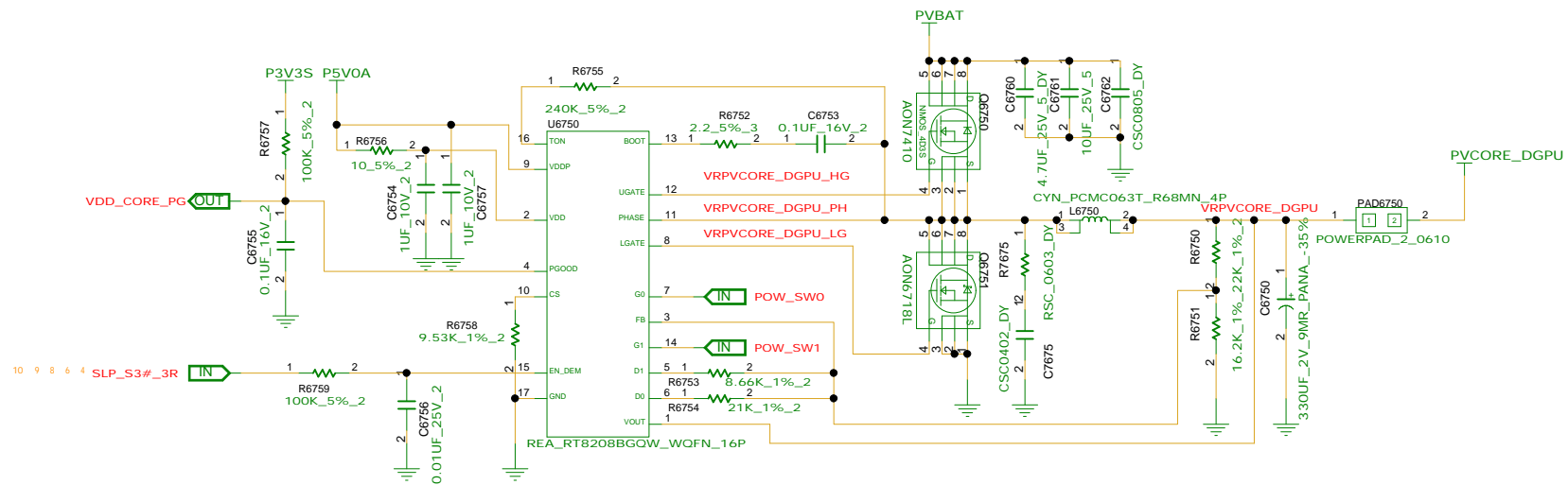
INVENTEC

TITLE MODEL,PROJECT,FUNCTION
SYSTEM POWER(PVCORE)

SIZE CODE DOC NUMBER REV
A3 CS 1310xxxxx-0-0 X01

CHANGE by First Chang DATE 23-MAY-2011

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INVENTEC

TITLE MODEL, PROJECT, FUNCTION
SYSTEM POWER(PVCORE_DGPU)

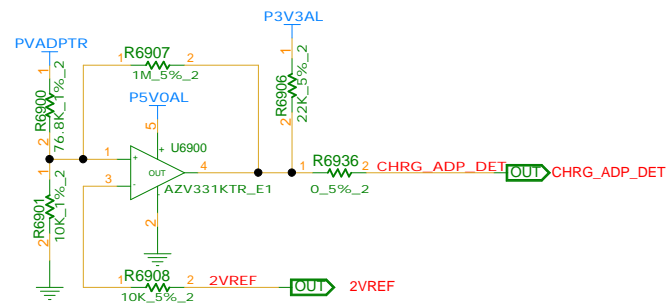
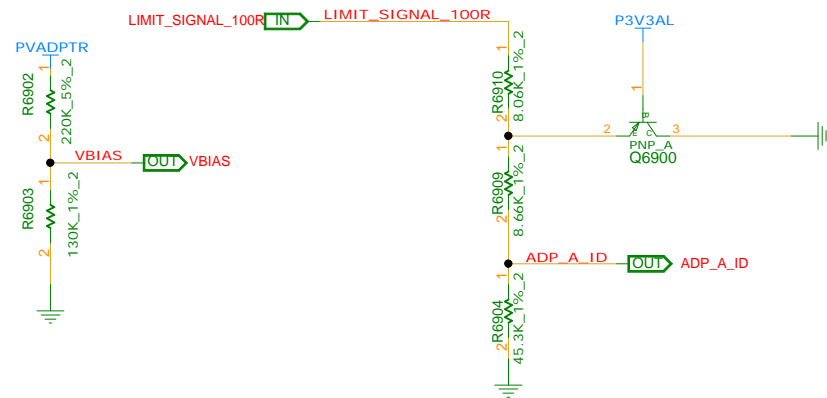
DOC NUMBER 1310xxxxx-0-0

REV X01

CHANGE by First Chang DATE 23-MAY-2011

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REFERENCE NUMBER : 6900~6999



INVENTEC

TITLE
MODEL PROJECT, FUNCTION

HP_OCP

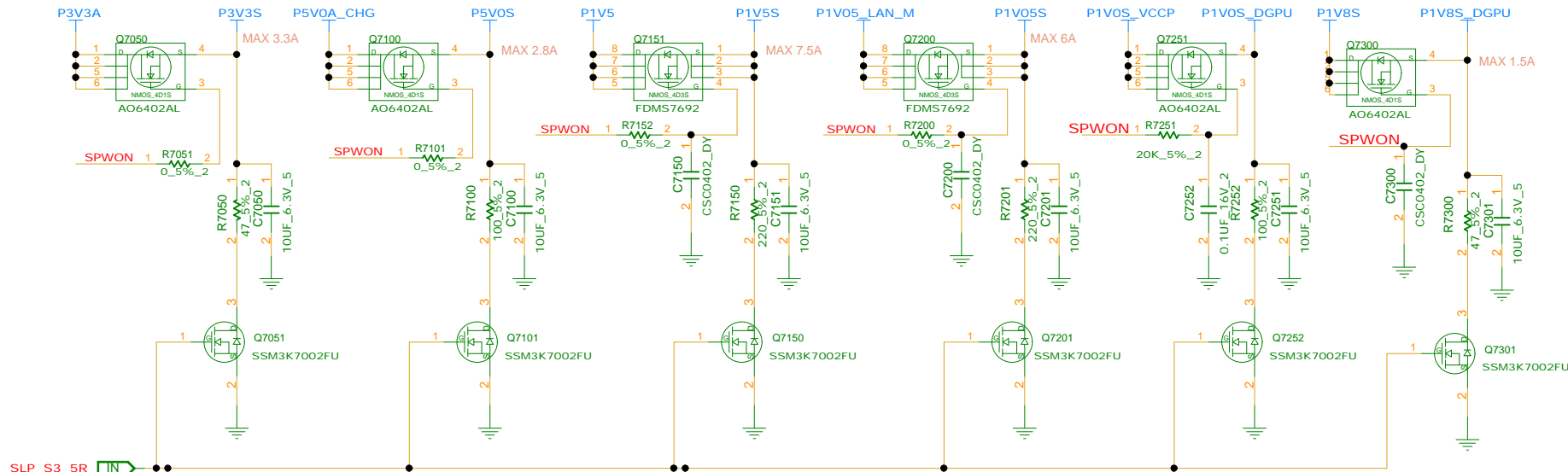
DOC NUMBER
1310xxxxx-0-0

REV
X01

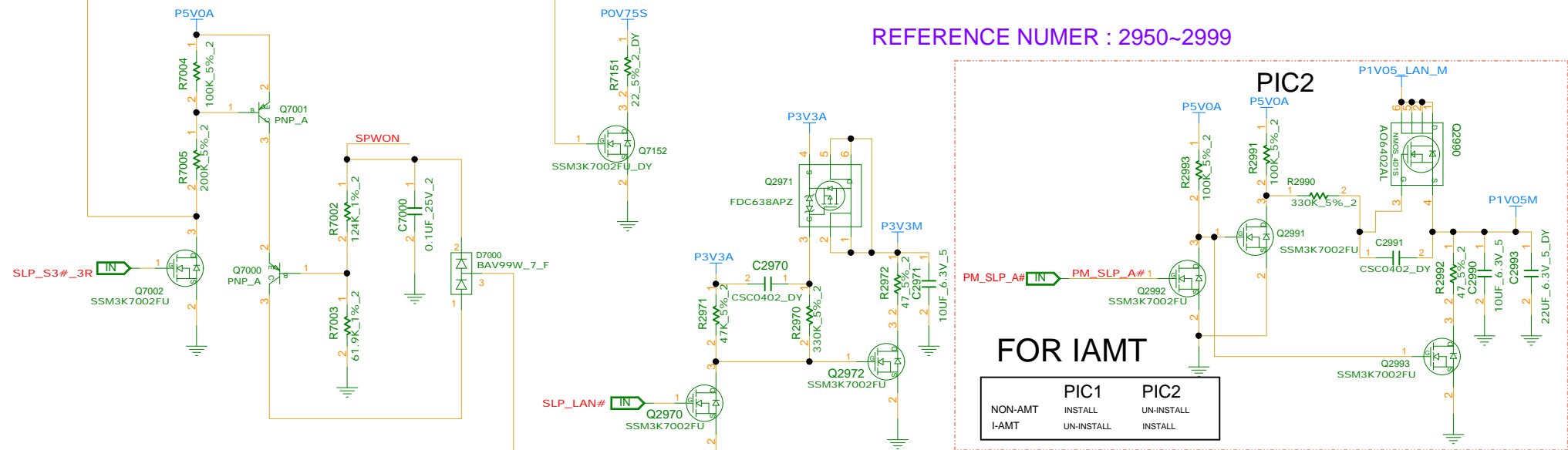
CHANGE by First Chang DATE 23-MAY-2011

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REFERENCE NUMER : 7000~7350

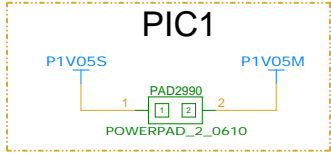


REFERENCE NUMER : 2950~2999



FOR I-AMT

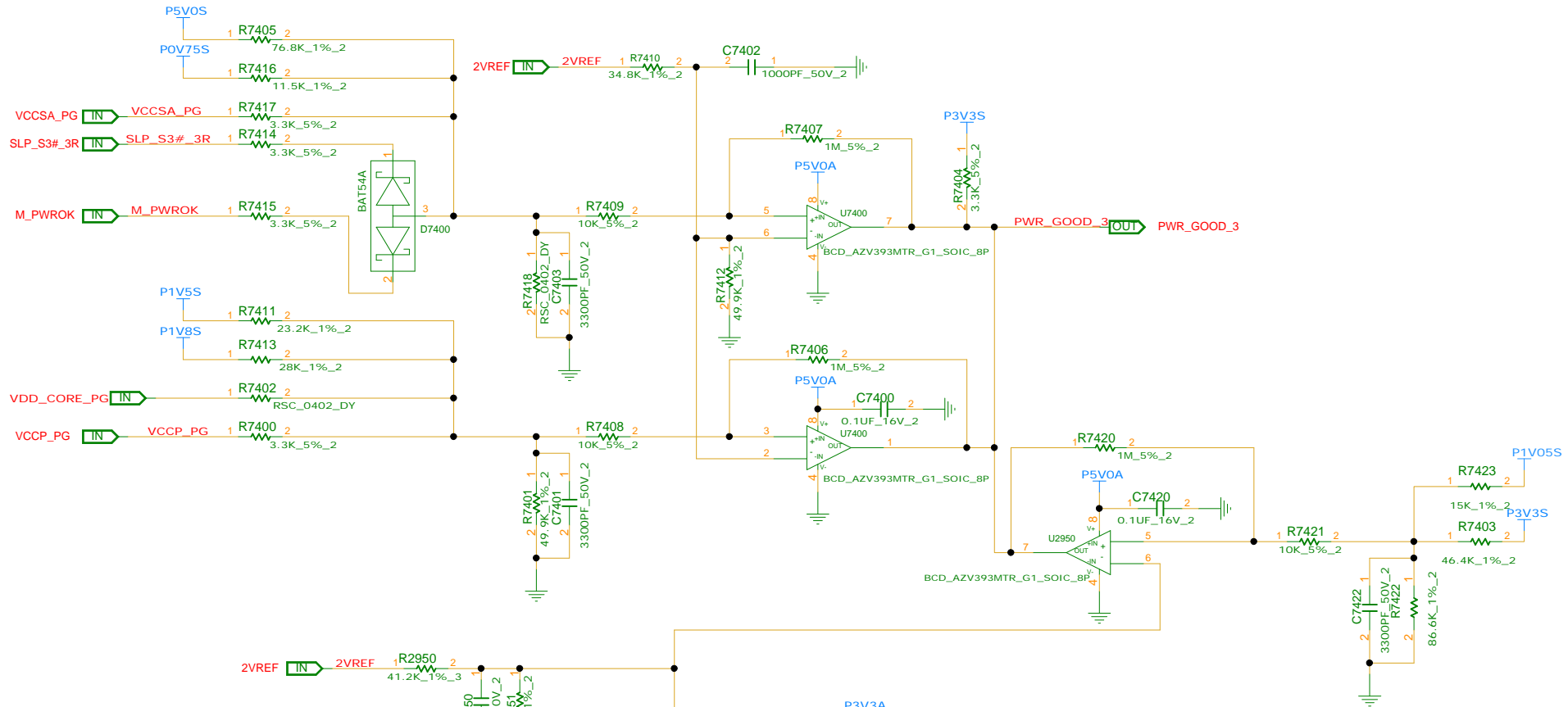
	PIC1	PIC2
NON-AMT	INSTALL	UN-INSTALL
I-AMT	UN-INSTALL	INSTALL



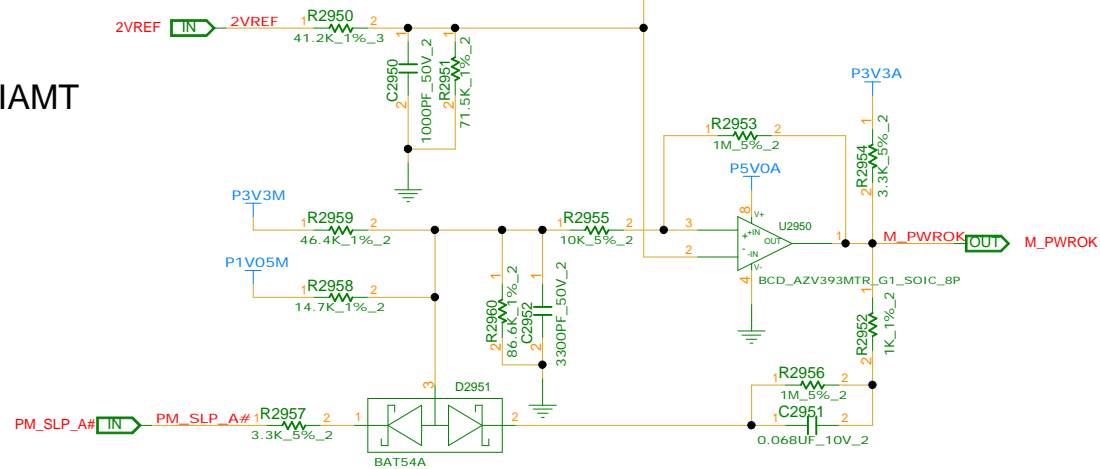
INVENTEC

TITLE			
MODEL PROJECT,FUNCTION			
POWER(SLEEP)			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

REFERENCE NUMER : 7400~7450



FOR IAMT

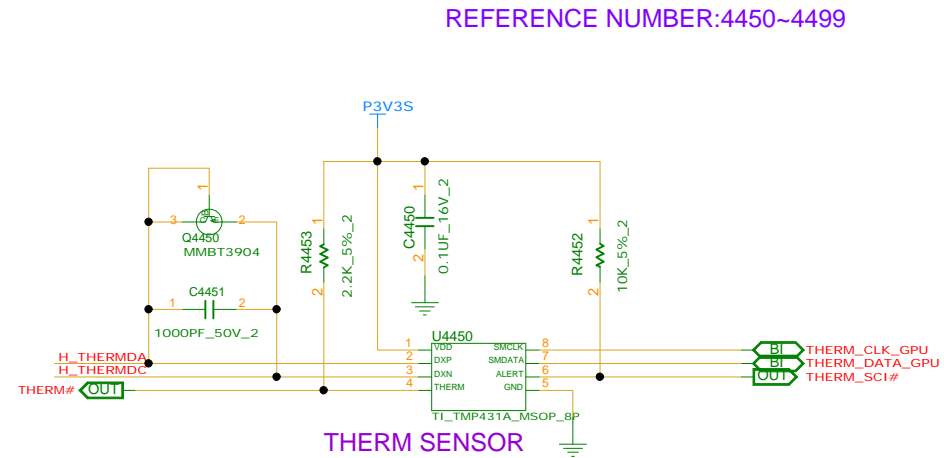
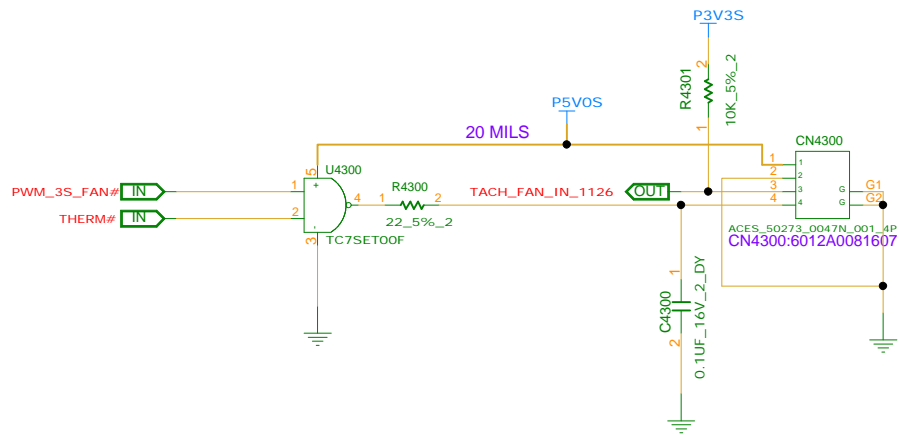


REFERENCE NUMER : 2950~2999

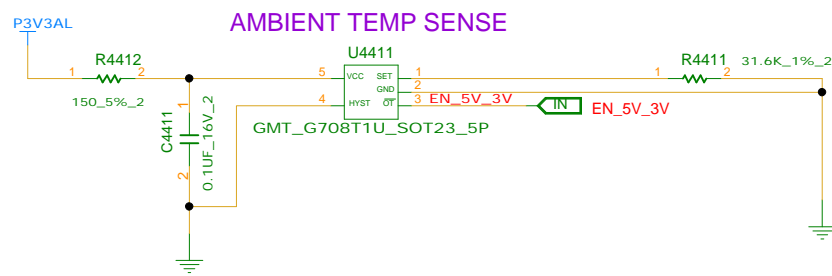
INVENTEC

TITLE			
MODEL PROJECT FUNCTION POWER (SEQUENCE)			
SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
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Fan speeds (RPM) :	2500	3300	3800	4300	5000	0	0	0
CPU temperature trip points :	45	57	68	80	91	0	0	0



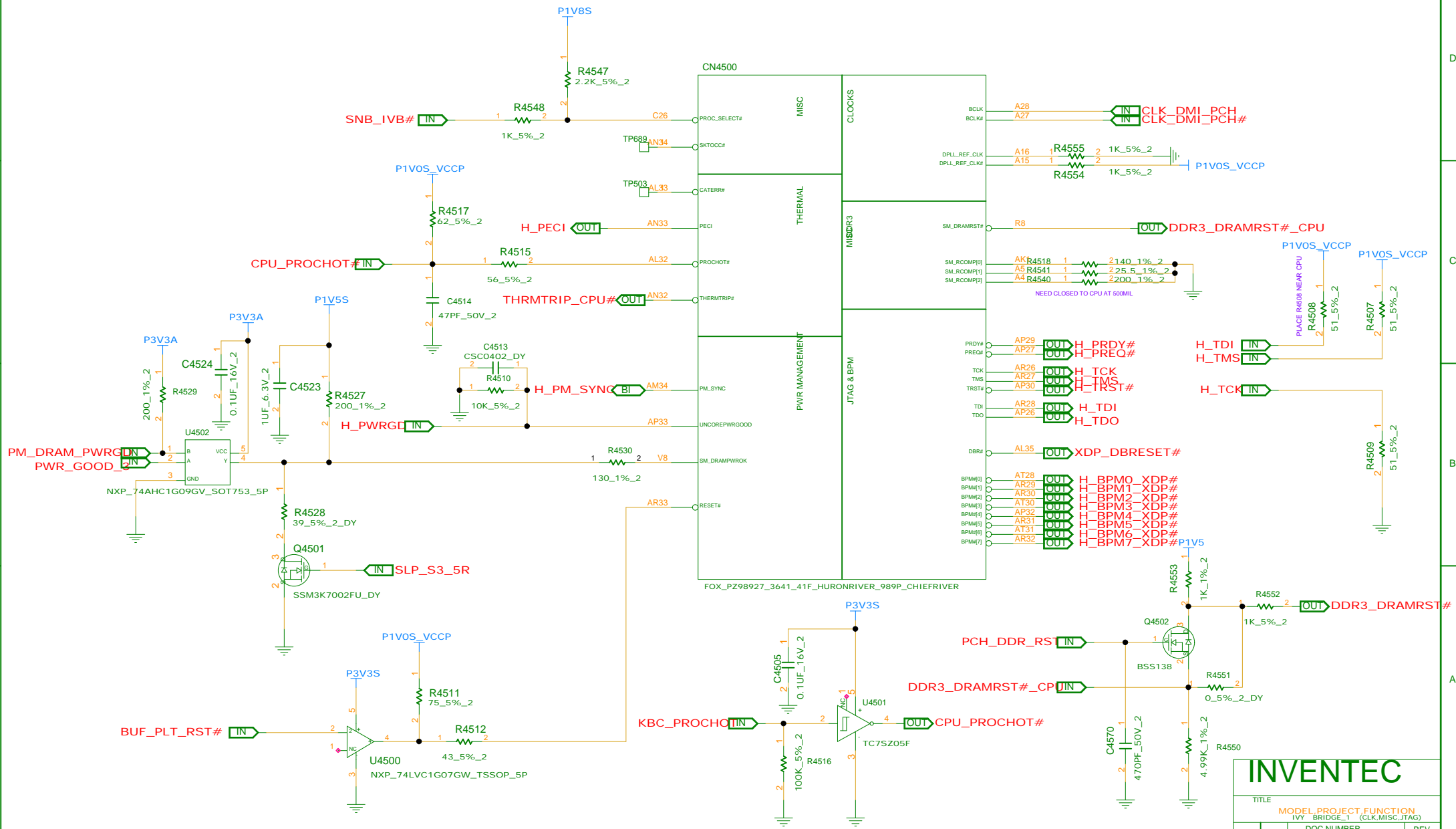
RSET (K OHM) = 0.0012T^2 - 0.9308T + 96.147
THERMAL SHUTDOWN AT 77 DEG. FOR B SERIES

REFERENCE NUMBER:4411~4449

INVENTEC			
TITLE			
MODEL PROJECT FUNCTION			
FAN & THERMAL			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
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CHANGE by First Chang DATE 23-MAY-2011

REFERENCE NUMBER:4500~4699



INVENTEC							
TITLE							
MODEL PROJECT FUNCTION							
IVY BRIDGE_1 (CLK,MISC,JTAG)							
DOC NUMBER							
1310xxxxx-0-0							
REV							
X01							
SIZE	CODE						
A3	CS						
CHANGE by First Chang							
DATE 23-MAY-2011							
SHEET 23 of 72							



REFERENCE NUMBER:4500~4699

PCI EXPRESS* - GRAPHICS

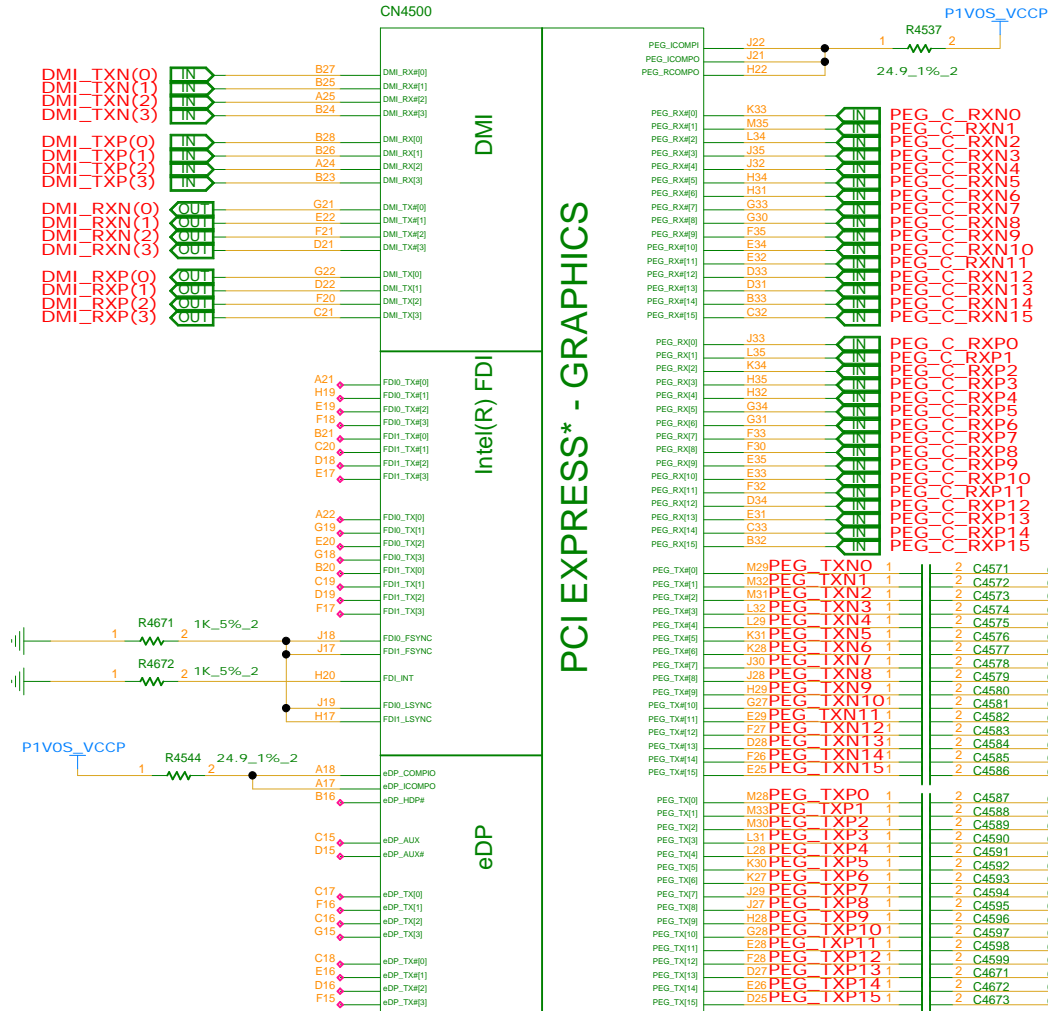
ALL CLOSED TO CPU

INVENTEC

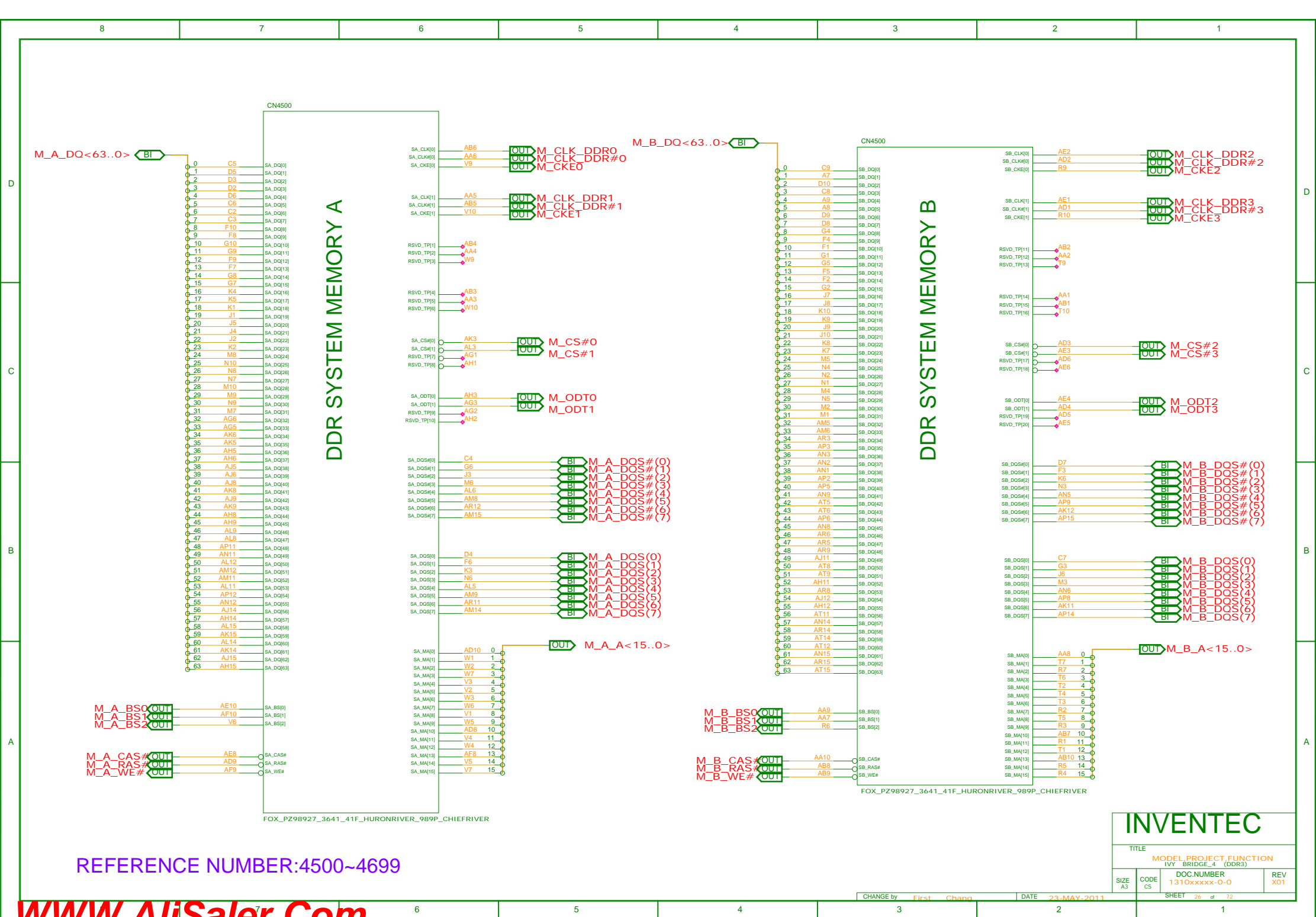
TITLE
MODEL PROJECT FUNCTION
IVY BRIDGE_3 (DMI,DP,PEG,FDI)

SIZE CODE DOC NUMBER REV
A3 CS 1310xxxxx-0-0 X01

CHANGE by First Chang DATE 23-MAY-2011 SHEET 25 of 72



FOX_PZ98927_3641_41F_HURONRIVER_989P_CHIEFRIVER

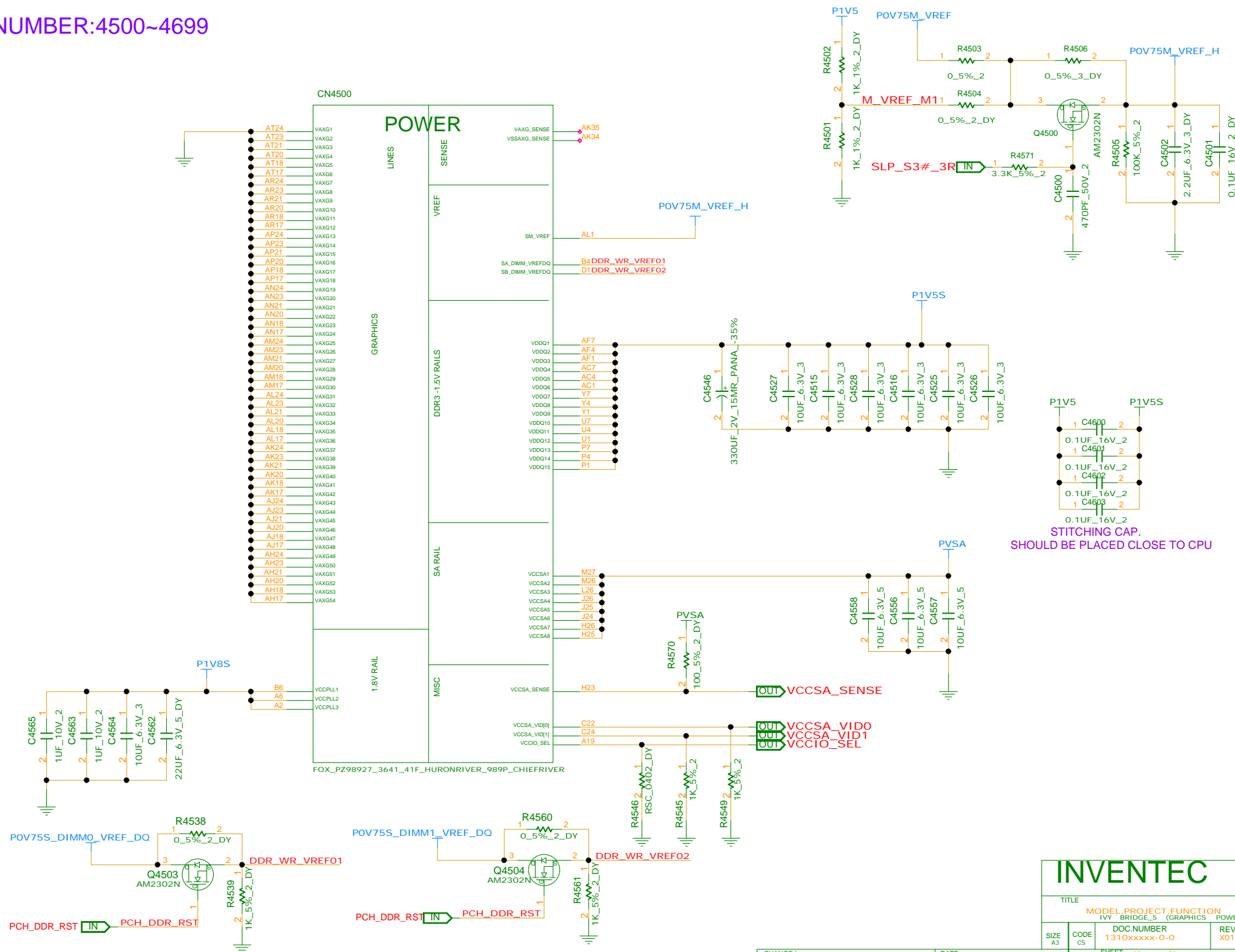


REFERENCE NUMBER:4500~4699

INVENTEC				
TITLE				
MODEL PROJECT FUNCTION				
IVY BRIDGE_4 (DDR3)				
SIZE A3	CODE CS	DOC NUMBER		REV X01
		1310xxxxx-0-0		
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CHANGE by First Chang DATE 23-MAY-2011

REFERENCE NUMBER:4500~4699

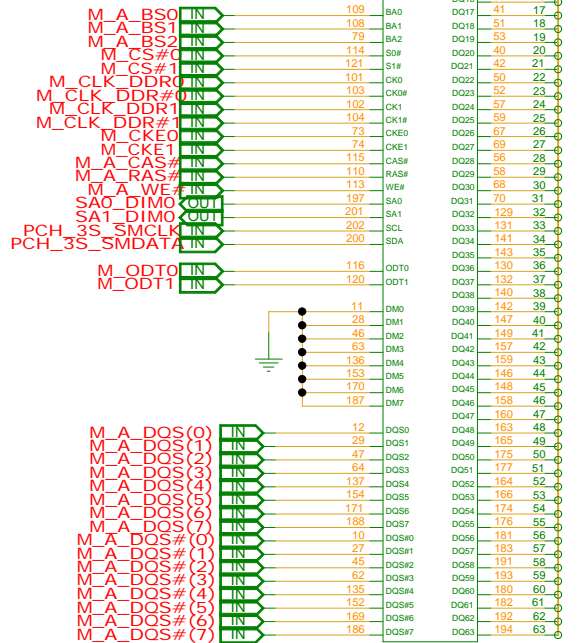


INVENTEC

TITLE			
MODEL PROJECT FUNCTION IVY BRIDGE_5 (GRAPHICS POWER)			
SIZE A3	CODE CS	DOC.NUMBER 1310xxxx-0-0	REV X01



M_A_A<15..0> BI M_A_DQ<63..0> BI



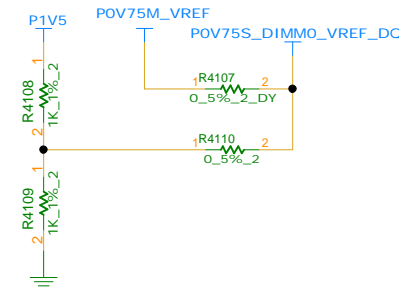
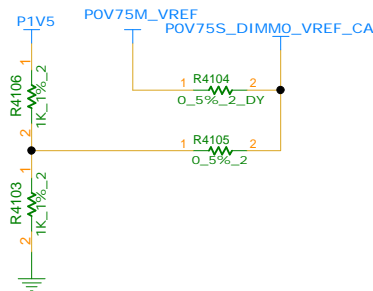
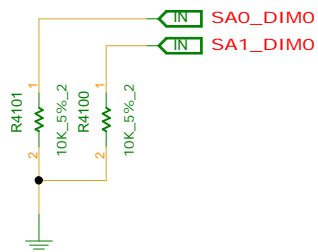
CN4650:6026B0162603

NOTE:

IF SA0_DIM0=0, SA1_DIM0=0
SO-DIMMA SPD ADDRESS IS 0XA0
SO-DIMMA TS ADDRESS IS 0X30

IF SA0_DIM0=1, SA1_DIM0=0
SO-DIMMA SPD ADDRESS IS 0XA2
SO-DIMMA TS ADDRESS IS 0X32

REFERENCE NUMBER:4100~4299

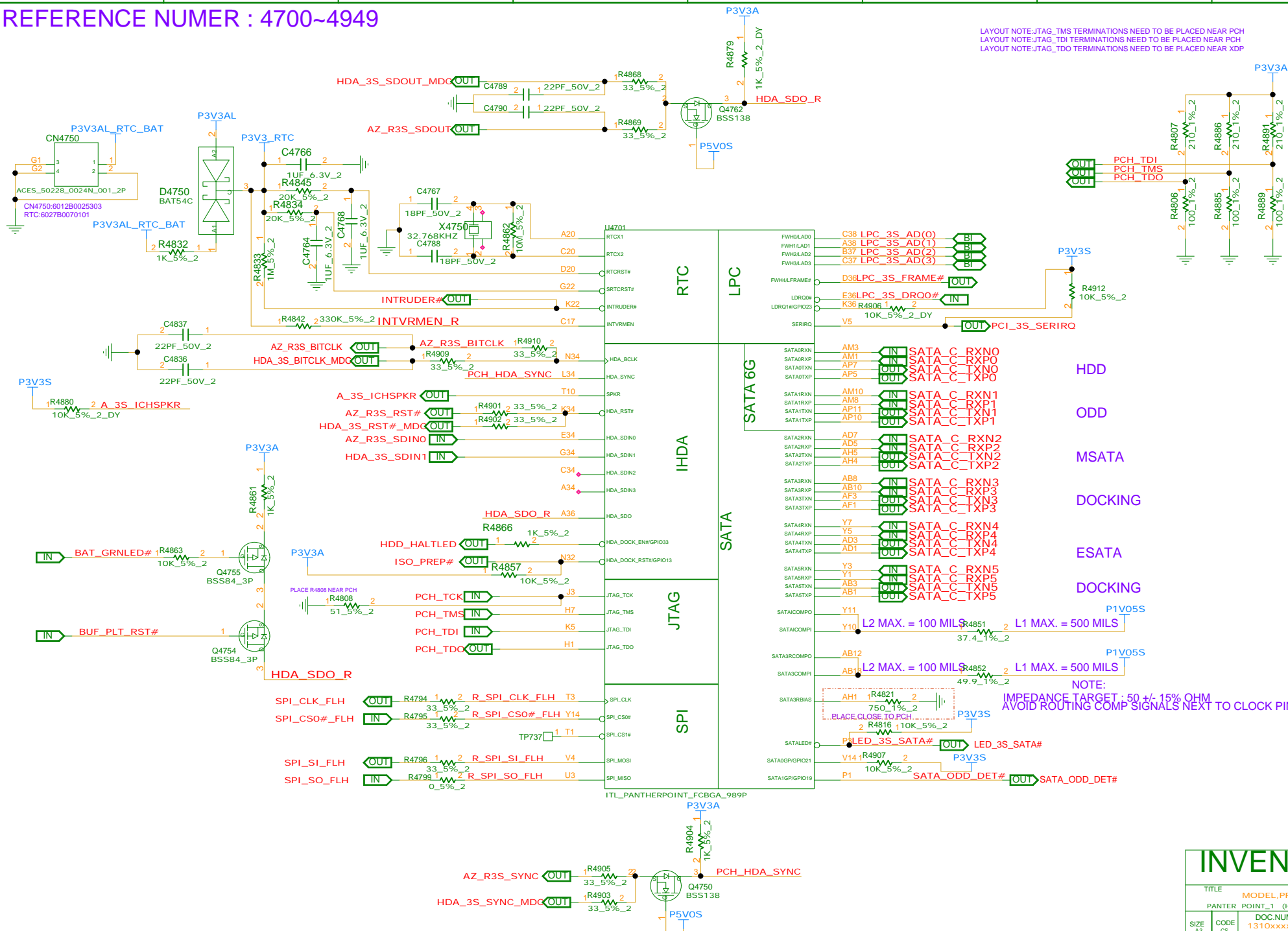


PM_EXTTS#1 R_OUT
DDR3_DRAMRST R_OUT

INVENTEC

TITLE			
MODEL PROJECT,FUNCTION			
DDR3_SO_DIMMO			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01

REFERENCE NUMBER : 4700~4949



LAYOUT NOTE: JTAG_TMS TERMINATIONS NEED TO BE PLACED NEAR PCH
LAYOUT NOTE: JTAG_TDI TERMINATIONS NEED TO BE PLACED NEAR PCH
LAYOUT NOTE: JTAG_TDO TERMINATIONS NEED TO BE PLACED NEAR XDP

HDD

ODD

MSATA

DOCKING

ESATA

DOCKING

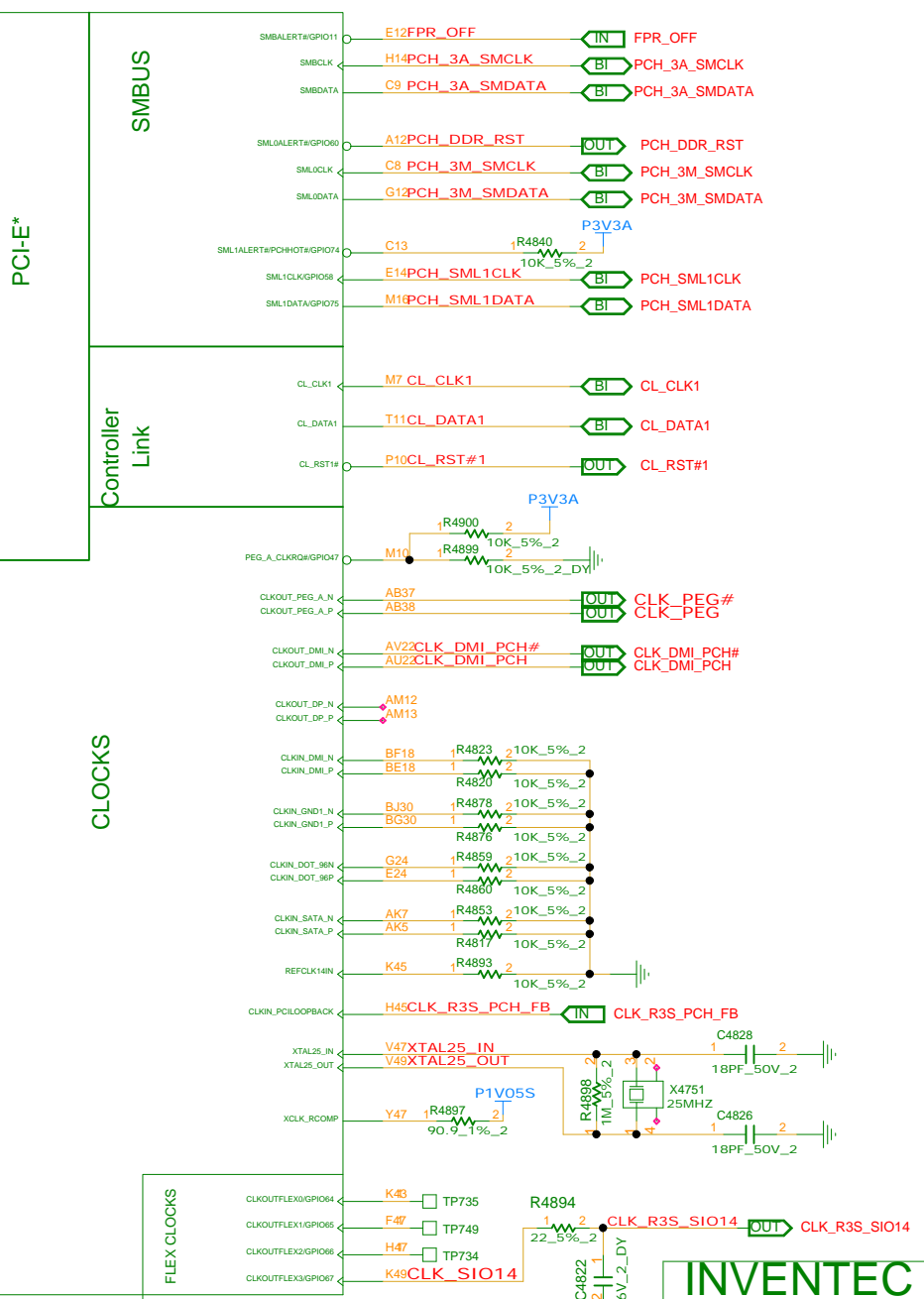
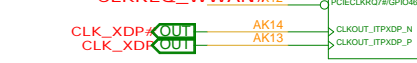
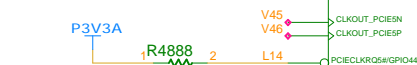
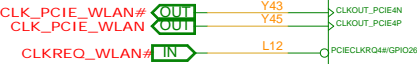
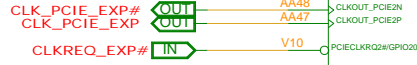
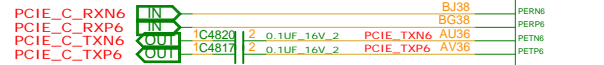
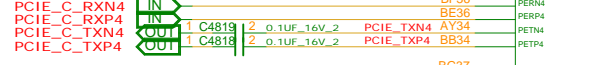
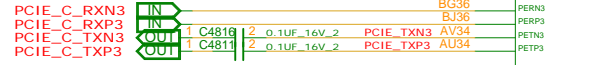
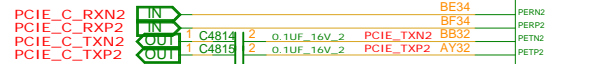
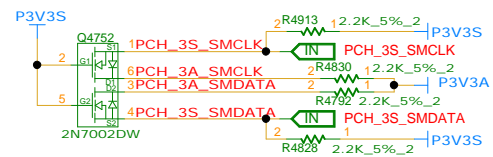
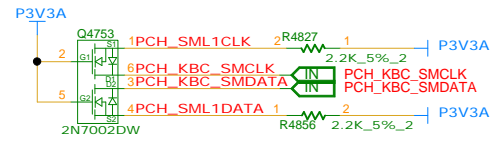
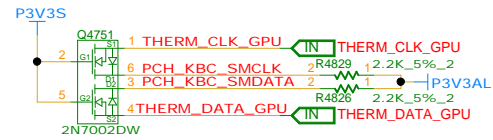
NOTE:
IMPEDANCE TARGET : 50 +/- 15% OHM
AVOID ROUTING COMP SIGNALS NEXT TO CLOCK PINS.

INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION			
PANTER POINT_1 (HDA,JTAG,SPI,SATA)			
SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0	REV X01

CHANGE by	First	Chang	DATE
			23-MAY-2011

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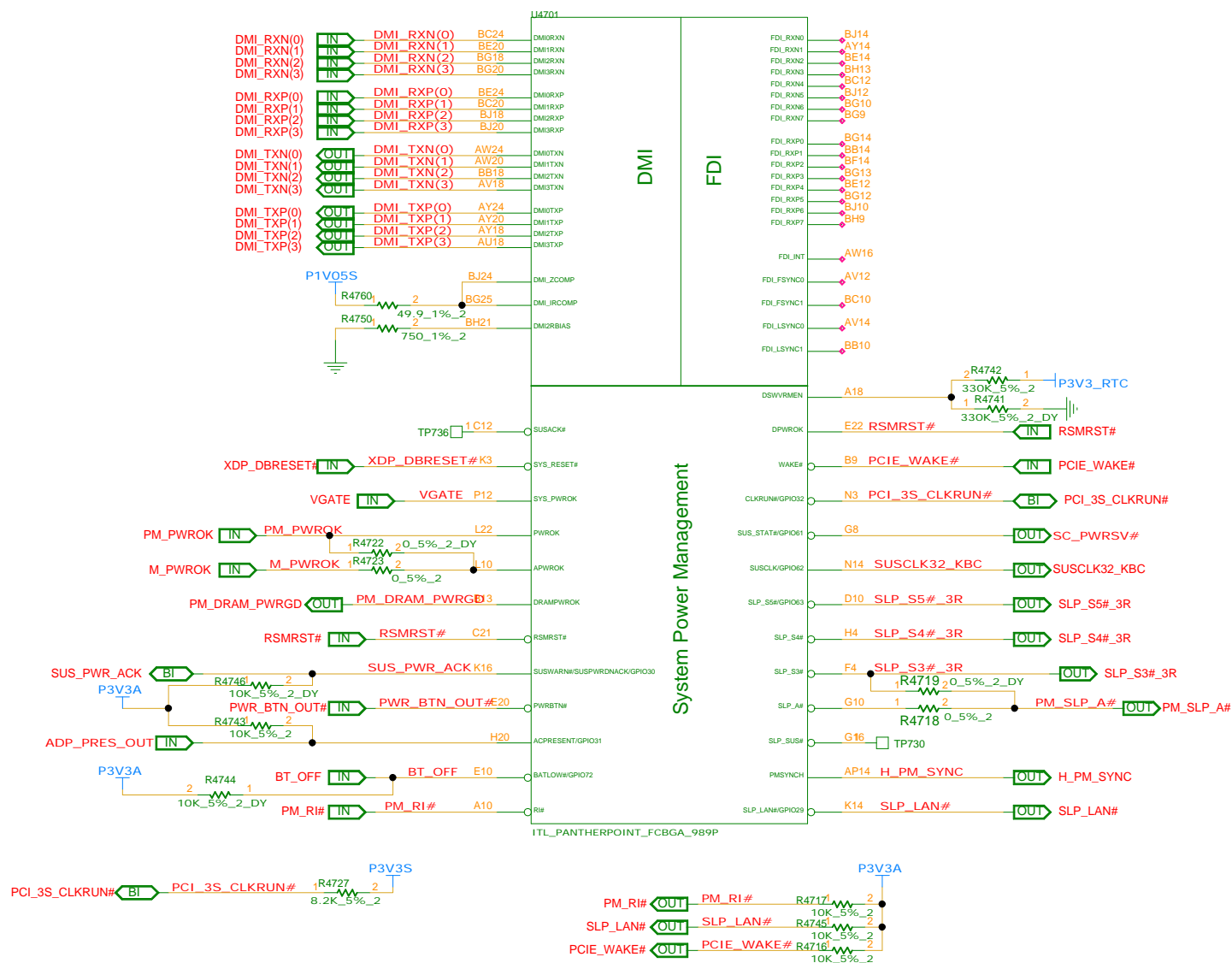
INVENTEC				
TITLE				
MODEL PROJECT FUNCTION PANTER POINT_2 (PCI-E, SMBUS, CLK)				
SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0		REV X01
SHEET 22 of 22				

REFERENCE NUMBER : 4700~4949

NOTE:

- NOTE:
- 1.SLP_SUS AND SUSACK# ARE NC IF DSW IS NOT SUPPORTED
 - 2.DPWROK SHOULD CONNECT TO RSMRST# IF DSW NOT SUPPORTED
 - 3.PCH_DPWROK PULL UP TO +V3S ENABLES DSW WUPPORT. NO INSTALL R5261 TO DISABLE DSW

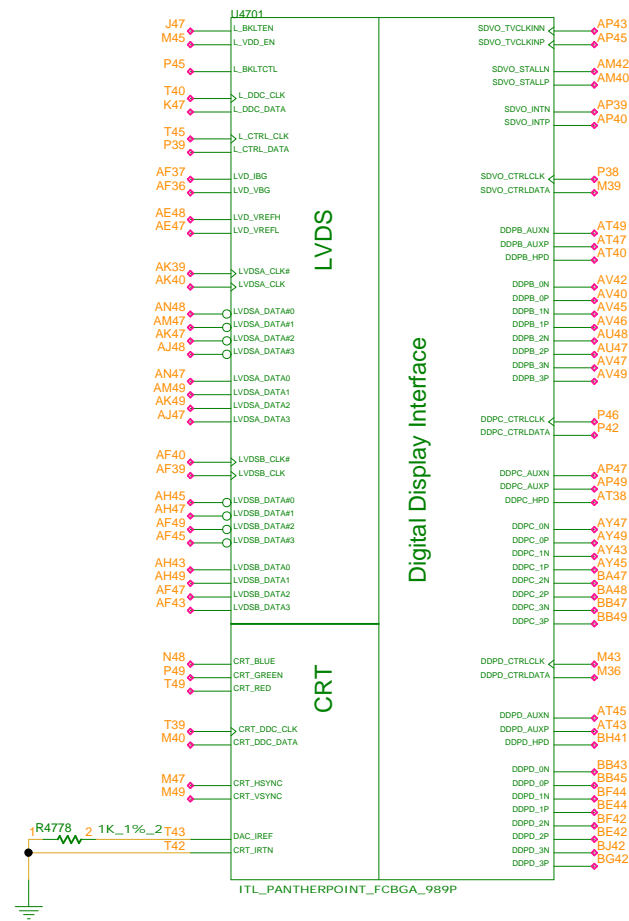
	IAMT	NON-IAMT
R4718	INSTALL	UNINSTALL
R4723	INSTALL	UNINSTALL
R4719	UNINSTALL	INSTALL
R4722	UNINSTALL	INSTALL



INVENTEC

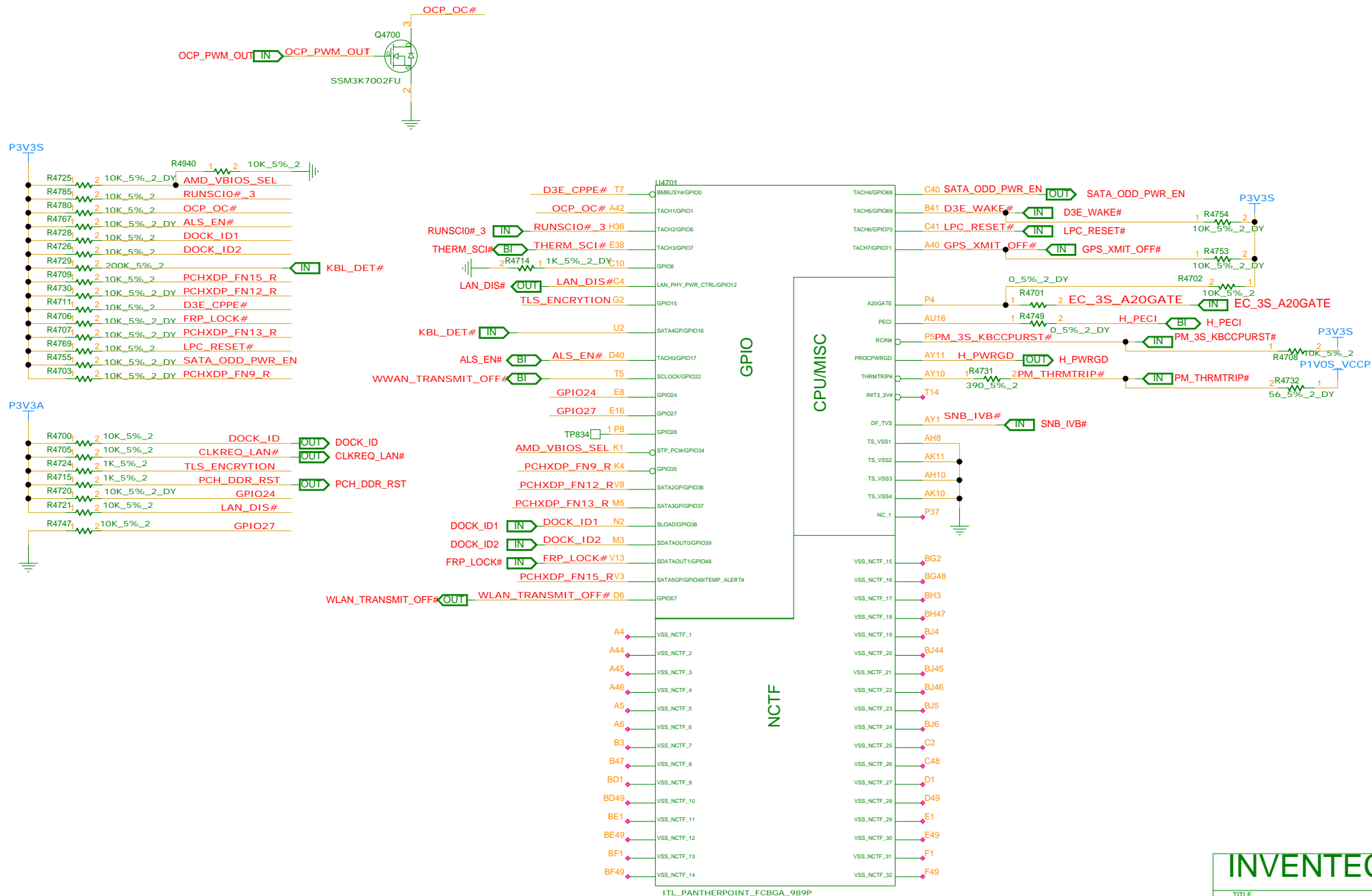
TITLE			
MODEL,PROJECT,FUNCTION PANTER POINT_3 (DMI,FDI,GPIO)			
SIZE A2	CODE CS	DOC.NUMBER 1310xxxxx-0-0	R X

REFERENCE NUMER : 4700~4949



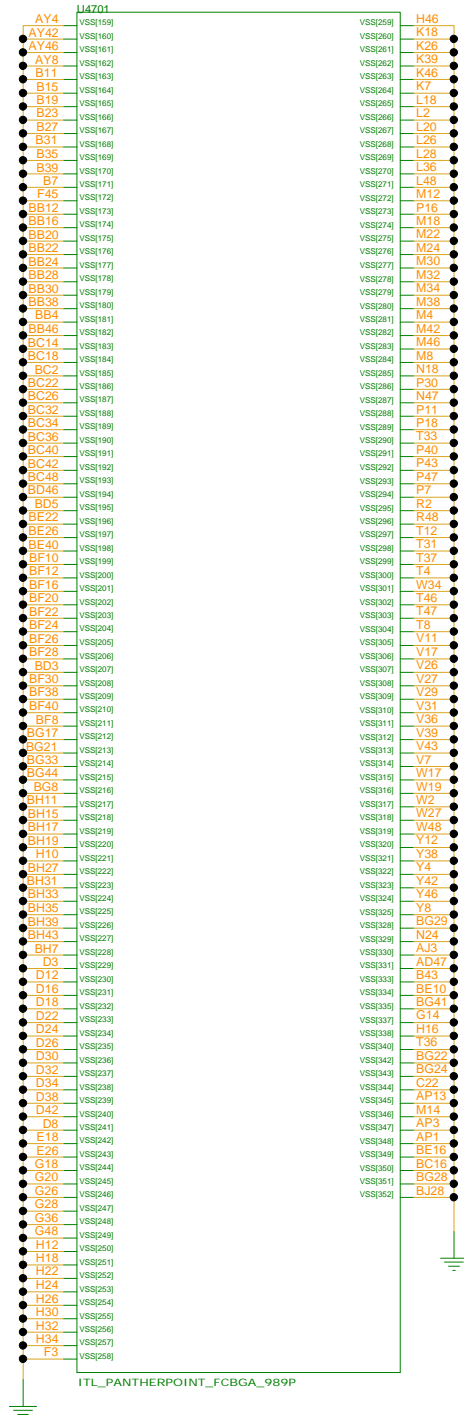
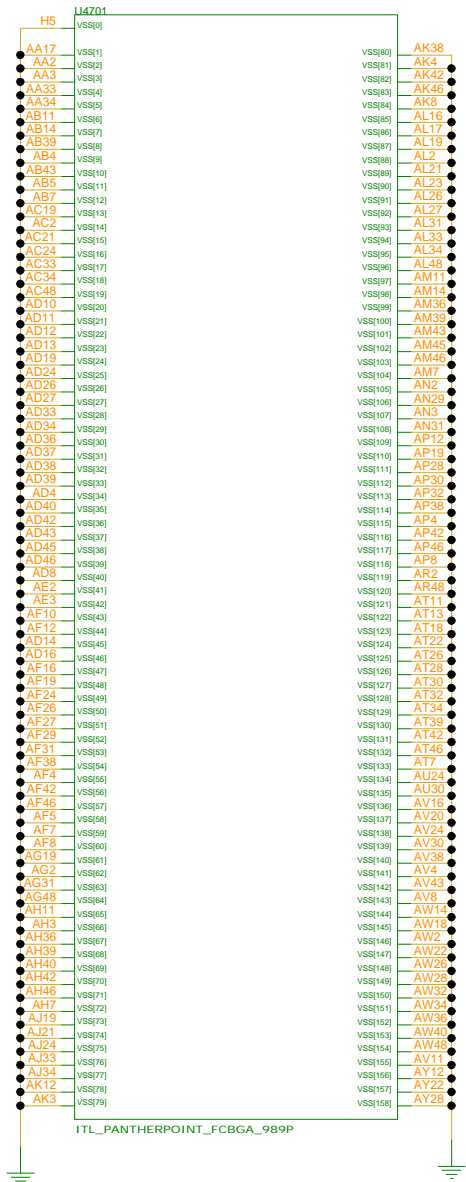
INVENTEC				
TITLE				
MODEL PROJECT FUNCTION PANTHER POINT_4 (LVDS,DDI)				
SIZE	CODE	DOC NUMBER	REV	
A3	CS	1310xxxxx-0-0	X01	

REFERENCE NUMBER : 4700~4949



INVENTEC			
TITLE			
MODEL PROJECT FUNCTION			
PANTER POINT_6 (GPIO,VSS,NCTF,RSVD)			
DOC NUMBER			
1310xxxxx-0-0			
REV			
X01			
SIZE	CODE	DATE	SHEET
A3	CS	23-MAY-2011	36 of 72

REFERENCE NUMBER : 4700~4949

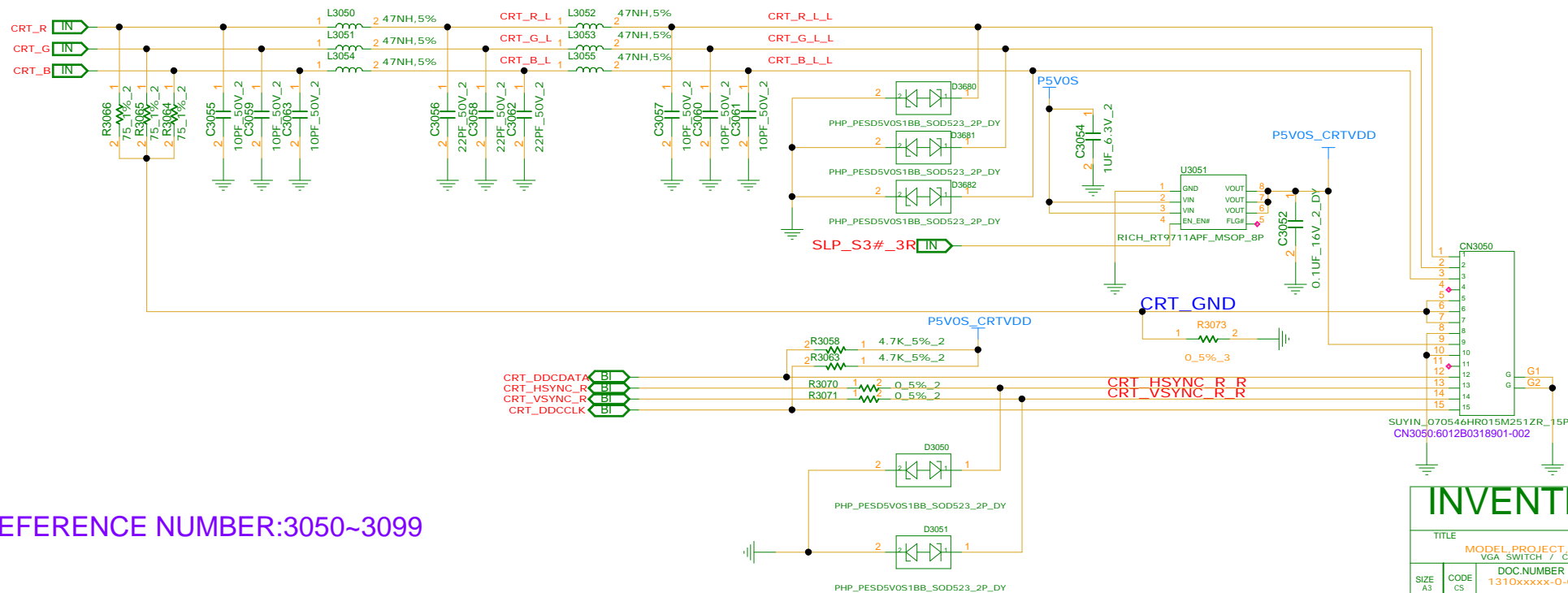
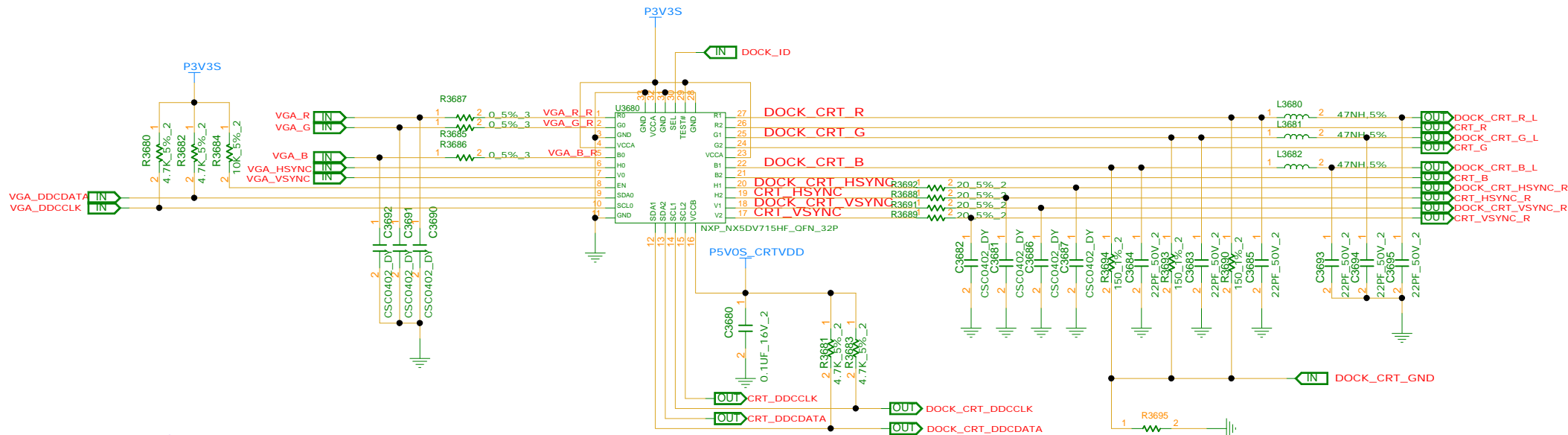


INVENTEC

TITLE
MODEL PROJECT FUNCTION
PANTER POINT-9 (GND)

SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
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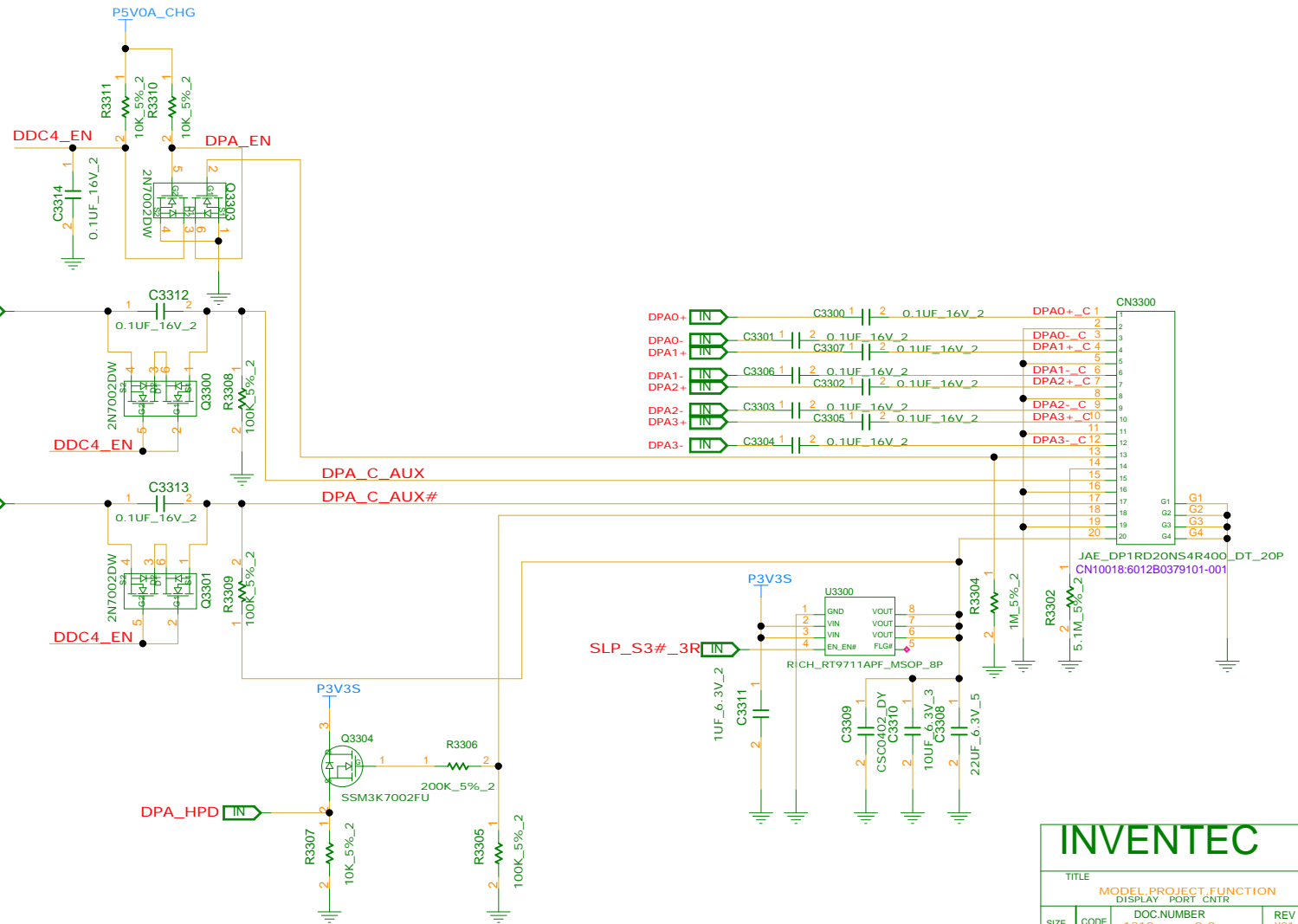
CHANGE by First Chang DATE 23-MAY-2011 SHEET 39 of 72



INVENTEC			
TITLE			
MODEL PROJECT FUNCTION			
VGA SWITCH / CRT			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
SHEET 40 of 72			

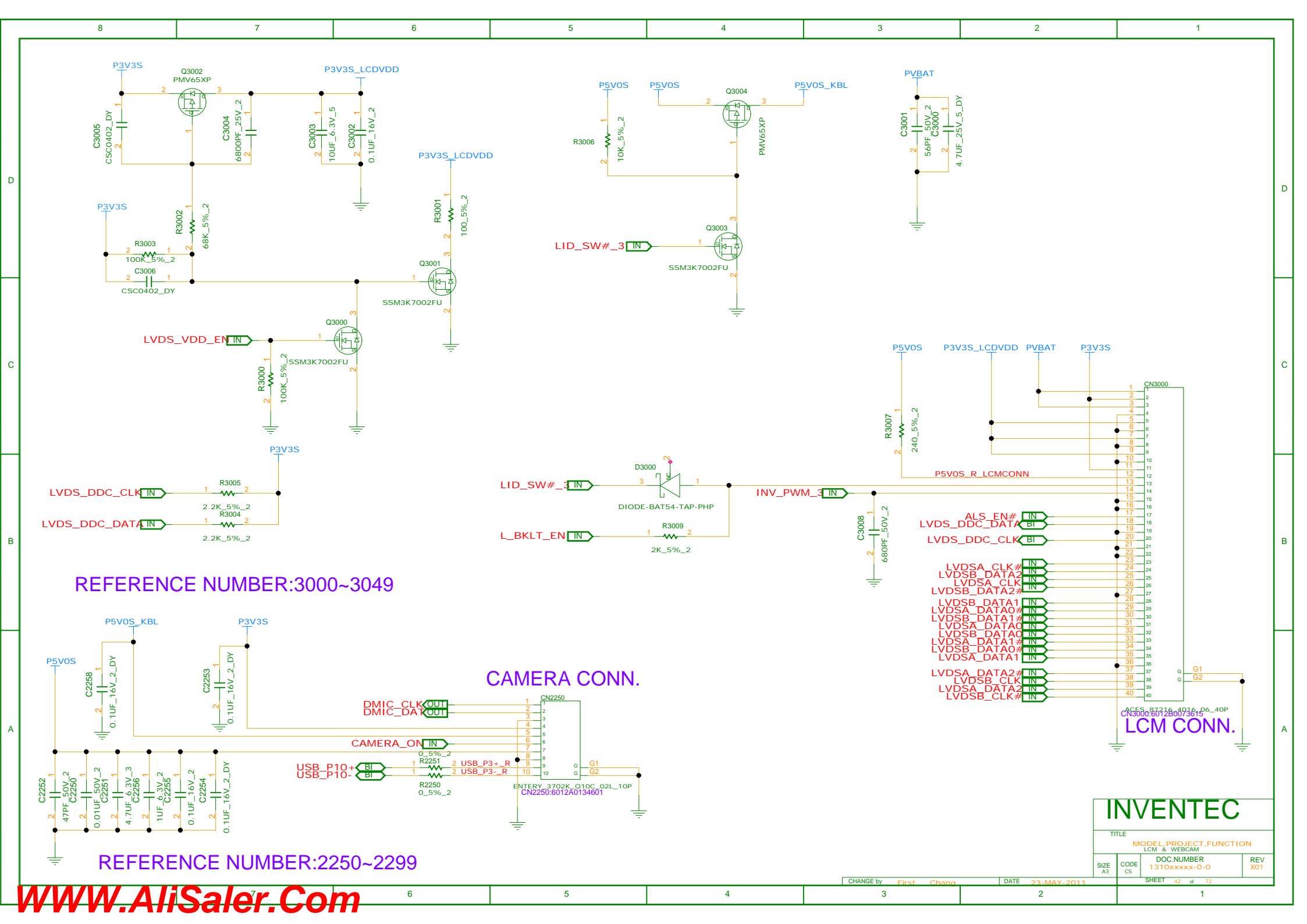
CHANGE by First Chang DATE 23-MAY-2011

REFERENCE NUMBER:3300~3399



INVENTEC

TITLE			
MODEL PROJECT.FUNCTION			
DISPLAY PORT CNTR			
DOC NUMBER			
1310xxxxx-0-0			
REV			
X01			



REFERENCE NUMBER:3000~3049

CAMERA CONN.

LCM CONN.

REFERENCE NUMBER:2250~2299

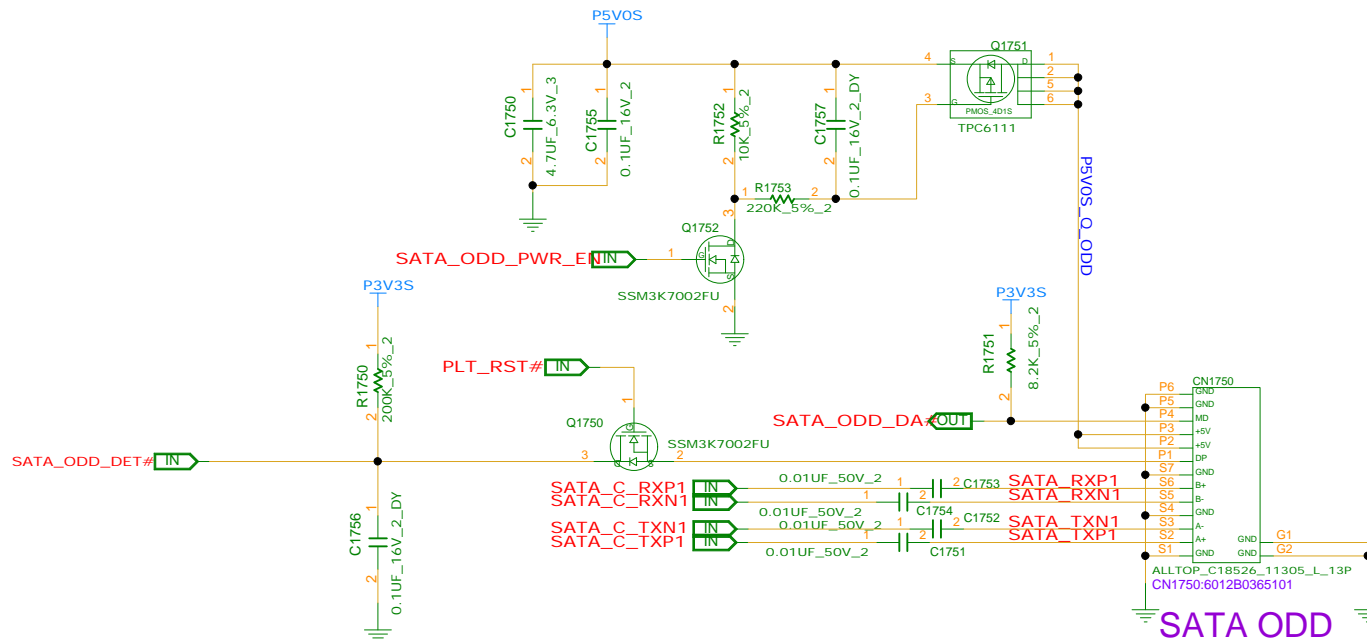
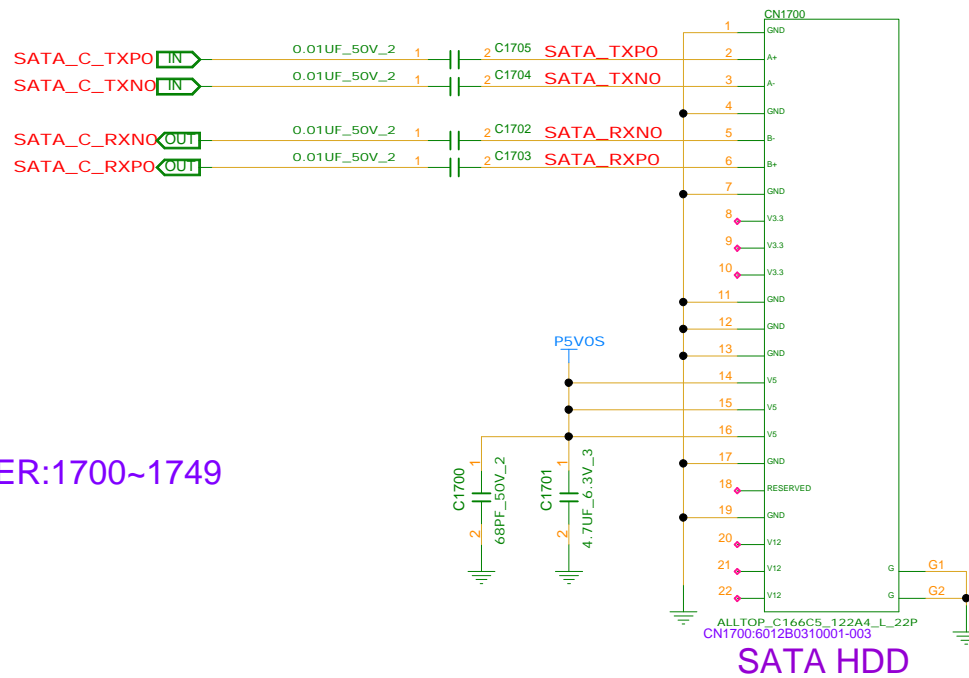
INVENTEC			
TITLE			
MODEL,PROJECT,FUNCTION			
LCM & WEBCAM			
DOC NUMBER			
1310xxxxx-0-0			
SIZE	CODE	REV	
A3	CS	X01	

BLANK

REFERENCE NUMBER:3400~3499

INVENTEC			
TITLE			
MODEL,PROJECT,FUNCTION			
EDP TO LVDS			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
SHEET 43 of 72			

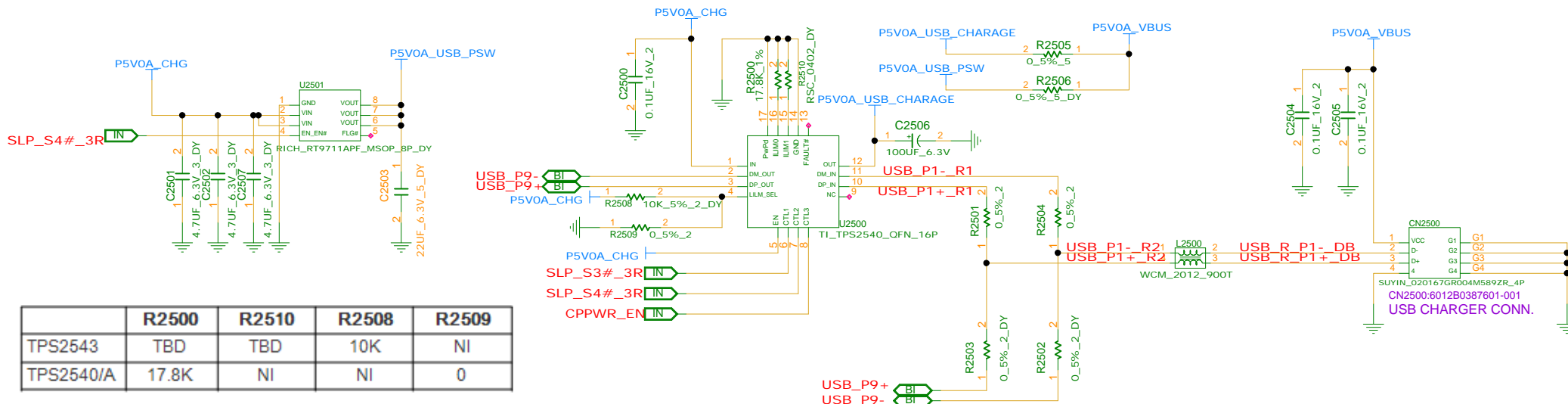
REFERENCE NUMBER:1700~1749



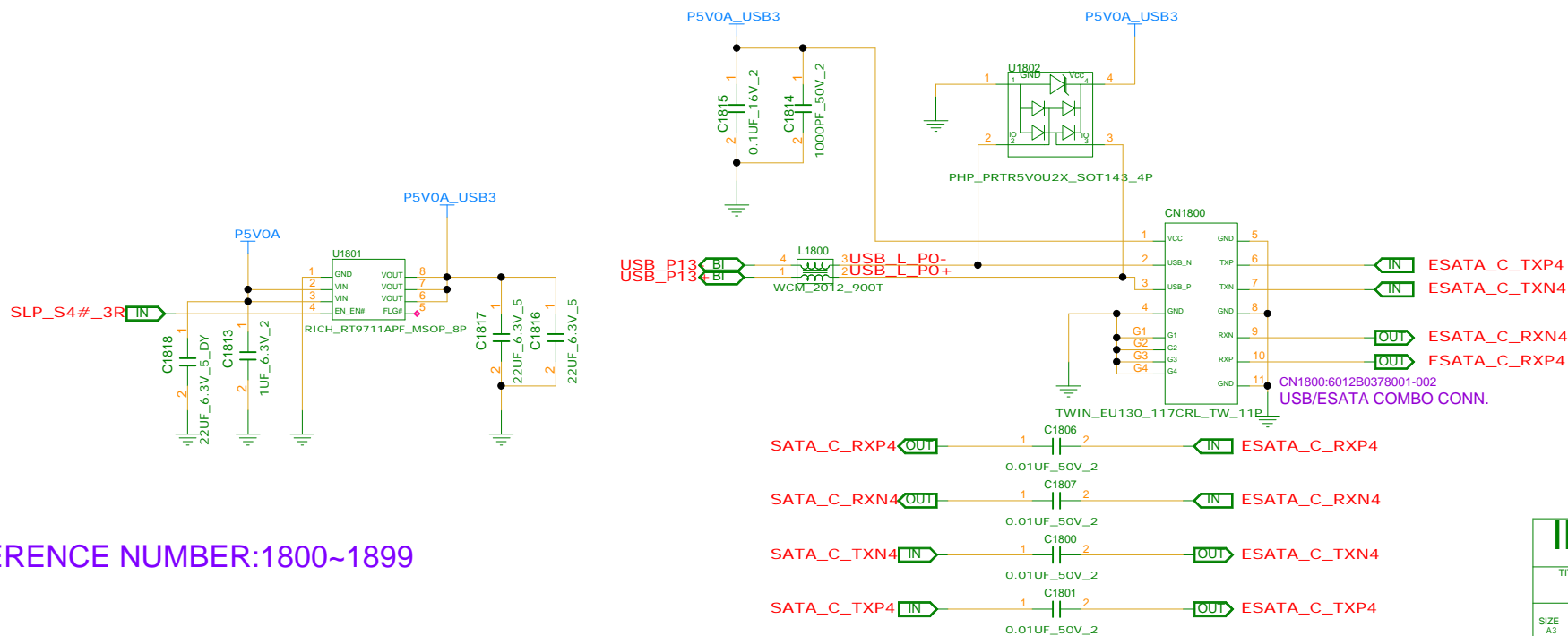
REFERENCE NUMBER:1750~1799

INVENTEC			
TITLE			
MODEL PROJECT FUNCTION			
SATA HDD & ODD CNTR			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
SHEET 44 of 72			

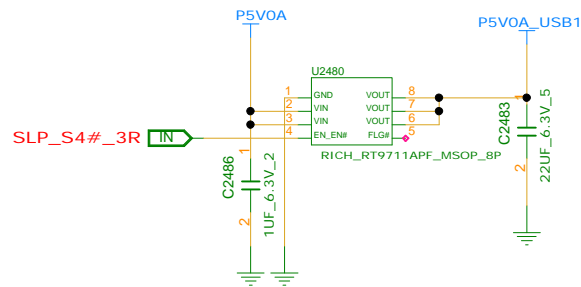
CHANGE by First Chang DATE 23-MAY-2011



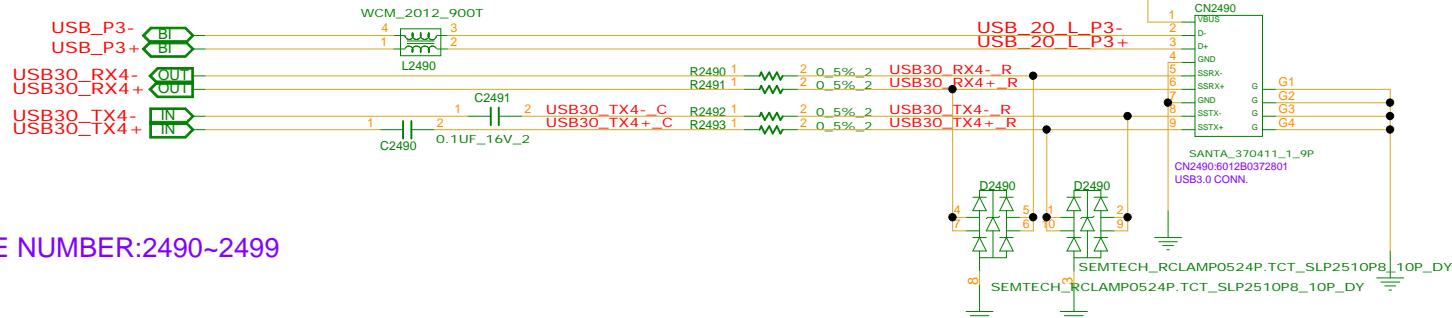
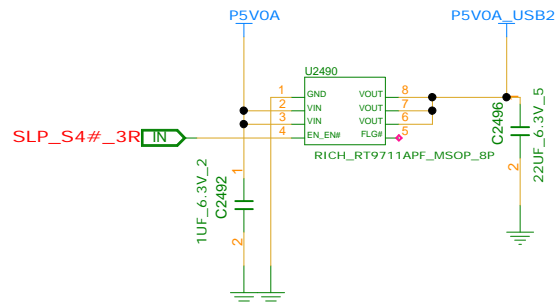
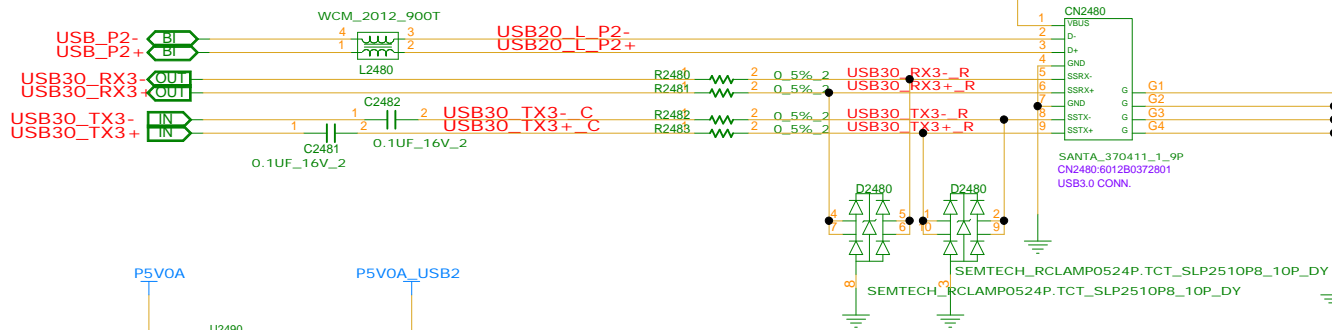
REFERENCE NUMBER:2500~2599



REFERENCE NUMBER:1800~1899



REFERENCE NUMBER:2480~2489

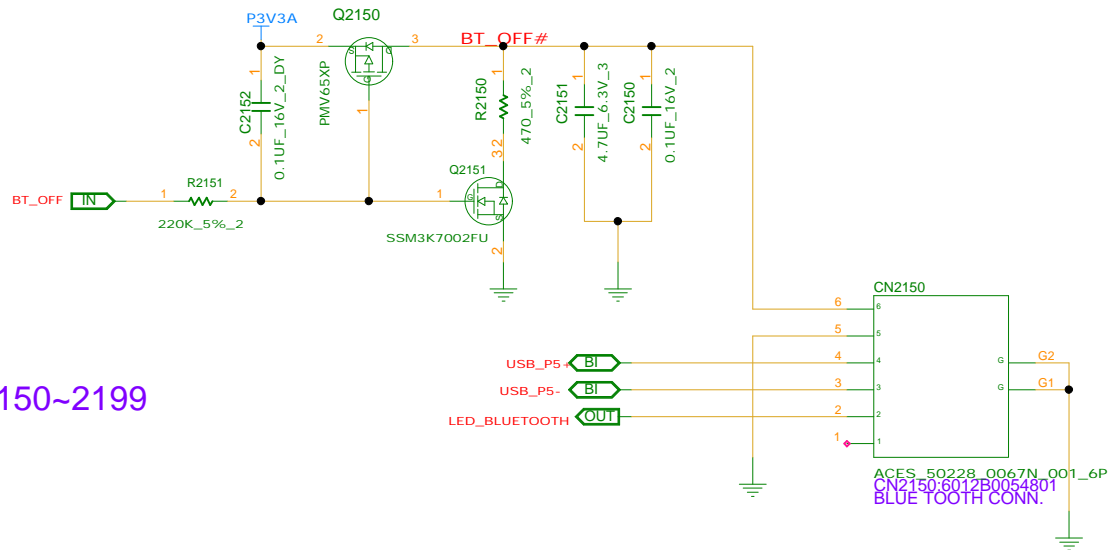


REFERENCE NUMBER:2490~2499

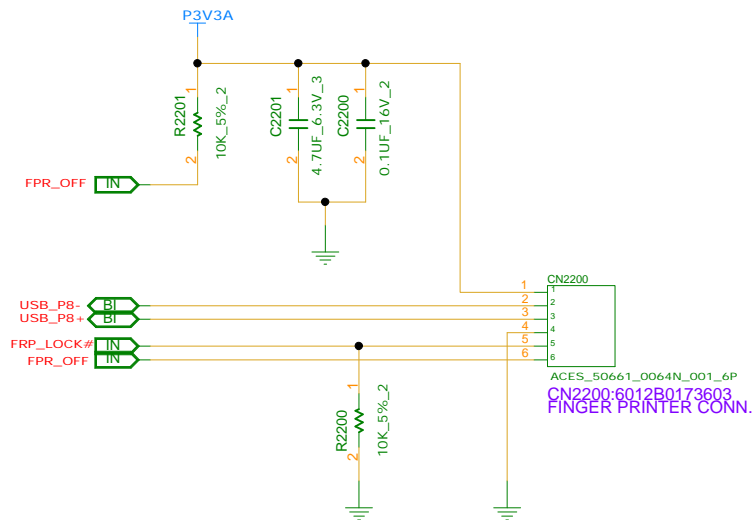
INVENTEC			
TITLE			
MODEL PROJECT FUNCTION			
USB CONN.			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
SHEET 46 of 72			

CHANGE by First Chang DATE 23-MAY-2011

REFERENCE:2150~2199



REFERENCE:2200~2249



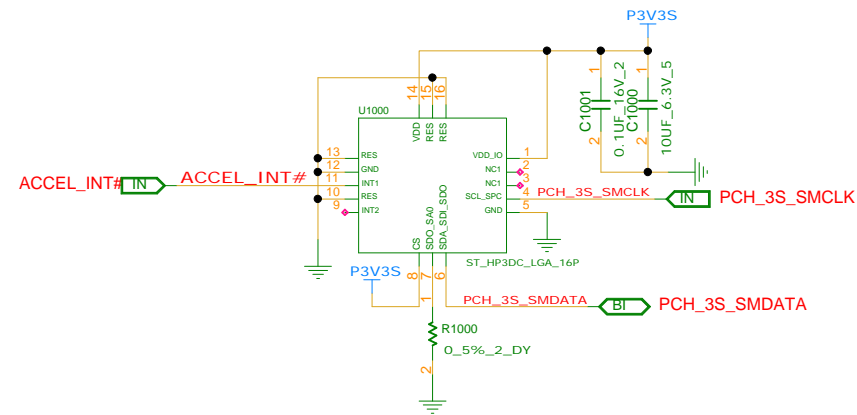
INVENTEC

TITLE			
BLUE TOOTH & FINGER PRINTER CNTR			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01

CHANGE by First Chang DATE 23-MAY-2011

SHEET 47 of 72

REFERENCE NUMER : 1000~1099



ACCELEMETOR

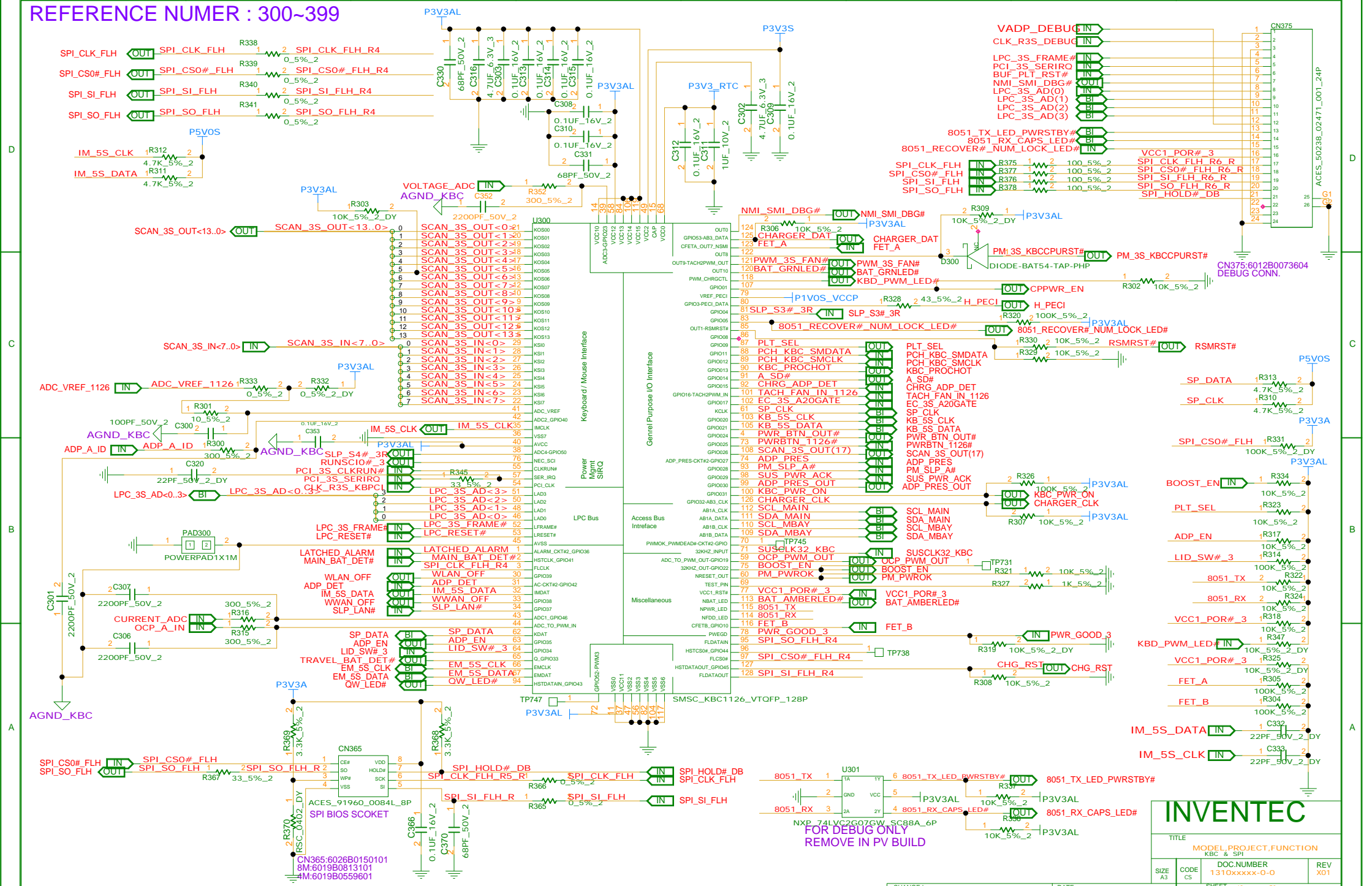
INVENTEC

TITLE
MODEL PROJECT,FUNCTION
ACCELEMETOR

SIZE A3 CODE CS DOC NUMBER 1310xxxxx-0-0 REV X01

CHANGE by First Chang DATE 23-MAY-2011 SHEET 48 of 72

REFERENCE NUMBER : 300~399



INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION KBC & SPI			
SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0	REV X01



SHEET 50 of 72

REFERENCE NUMER : 3700~3799

P5V0S
D3700
DIODE-BAT54-TAP-PHP

R3700 4.7K_5%
R3703 4.7K_5%
R3702 4.7K_5%
R3701 4.7K_5%

LPT_5S_SLCT
LPT_5S_PE
LPT_5S_BUSY
LPT_5S_ACK#
LPT_5S_ERROR#
LPT_5S_ALF#
LPT_5S_STRB#
UART_3S_RXD
UART_3S_TXD
UART_3S_DSR#

LPC_3S_AD<0>
LPC_3S_AD<1>
LPC_3S_AD<2>
LPC_3S_AD<3>

UART_3S_RTS#
UART_3S_CTS#
UART_3S_DTR#
UART_3S_RI#
UART_3S_DCD#

CLK_R3S_SIO14
LPC_3S_AD<0>
LPC_3S_AD<1>
LPC_3S_AD<2>
LPC_3S_AD<3>
LPC_3S_FRAME#
LPC_3S_DRQ0#

LPC_RESET#
LPC_RESET#
LPCPD#
PCI_3S_CLKRUN#
CLK_R3S_SIOPCI
PCI_3S_SERIRQ

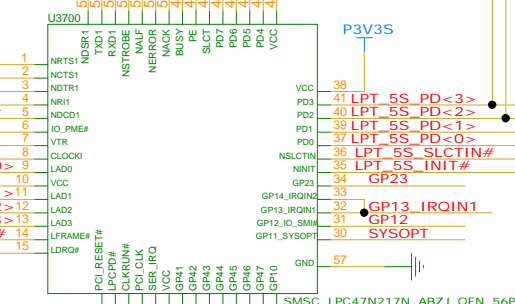
UART_3S_RTS#
UART_3S_DSR#
UART_3S_TXD
UART_3S_RXD

UART_3S_RTS#
UART_3S_DSR#
UART_3S_TXD
UART_3S_RXD

UART_3S_RTS#
UART_3S_DSR#
UART_3S_TXD
UART_3S_RXD

UART_3S_RTS#
UART_3S_DSR#
UART_3S_TXD
UART_3S_RXD

UART_3S_RTS#
UART_3S_DSR#
UART_3S_TXD
UART_3S_RXD



GP10
GPIO47
GP46
GP45
GP44
GP43
GP42
GP41

GP10
GPIO47
GP46
GP45
GP44
GP43
GP42
GP41

GP10
GPIO47
GP46
GP45
GP44
GP43
GP42
GP41

GP10
GPIO47
GP46
GP45
GP44
GP43
GP42
GP41

GP10
GPIO47
GP46
GP45
GP44
GP43
GP42
GP41

GP10
GPIO47
GP46
GP45
GP44
GP43
GP42
GP41

LPT_5S_PD<7>
LPT_5S_PD<6>
LPT_5S_PD<5>
LPT_5S_PD<4>

LPT_5S_PD<3>
LPT_5S_PD<2>
LPT_5S_PD<1>
LPT_5S_PD<0>

LPT_5S_SLCTIN#
LPT_5S_INIT#

GP23
GP41
GP42
SYSOPT

GP23
GP41
GP42
SYSOPT

GP23
GP41
GP42
SYSOPT

GP23
GP41
GP42
SYSOPT

GP23
GP41
GP42
SYSOPT

GP23
GP41
GP42
SYSOPT

GP23
GP41
GP42
SYSOPT

INVENTEC

TITLE

MODEL, PROJECT, FUNCTION

DOC NUMBER

REV

SIZE CODE CS

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D



B



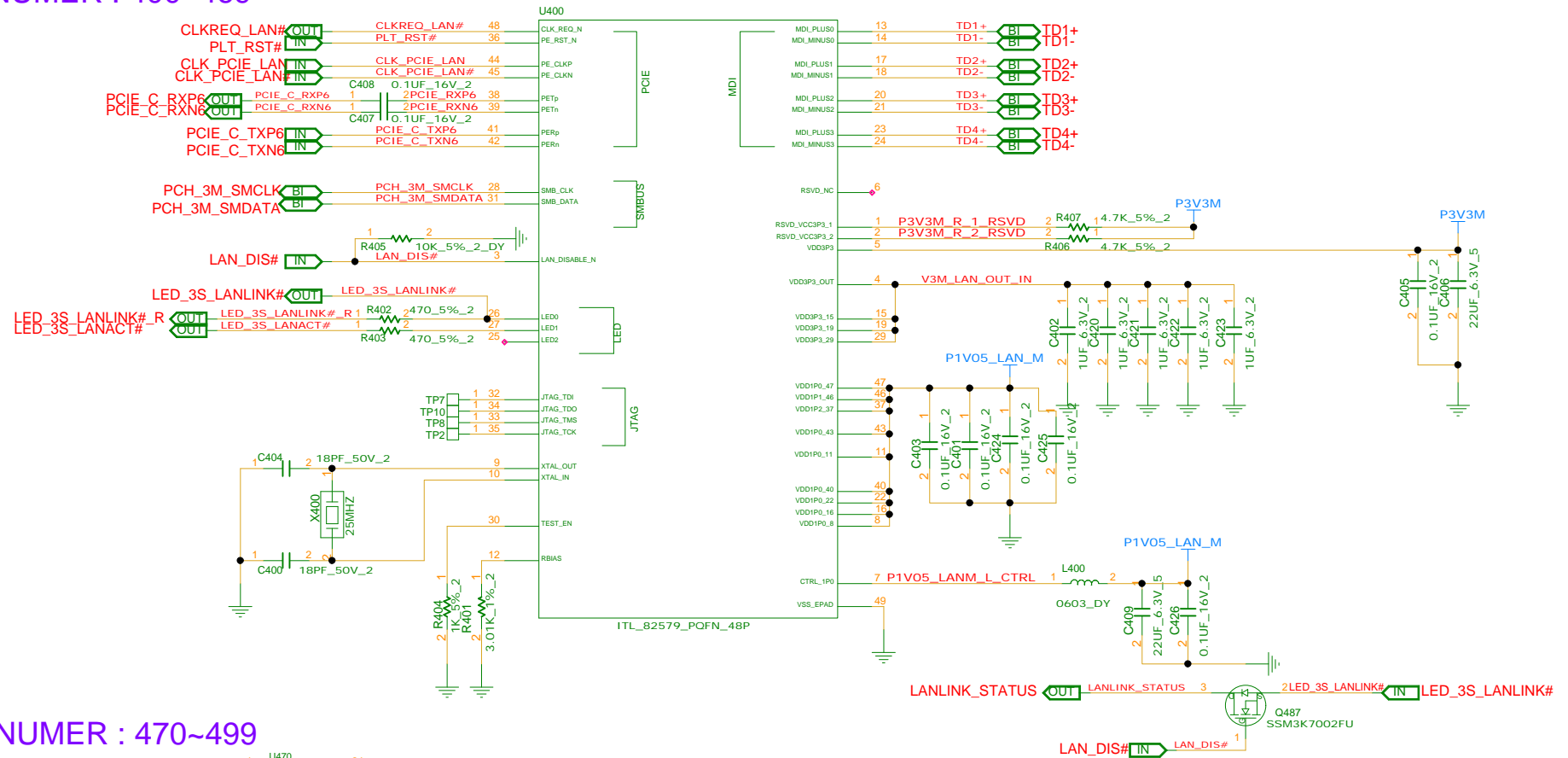
TITLE	MODEL PROJECT FUNCTION
1. Project Overview	1. Project Overview
2. Project Objectives	2. Project Objectives
3. Project Scope	3. Project Scope
4. Project Organization	4. Project Organization
5. Project Schedule	5. Project Schedule
6. Project Budget	6. Project Budget
7. Project Risks	7. Project Risks
8. Project Communication	8. Project Communication
9. Project Monitoring and Control	9. Project Monitoring and Control
10. Project Closure	10. Project Closure

MODEL, PROJECT, FUNCTION			
TPM & MDC			
	CODE	DOC. NUMBER	R

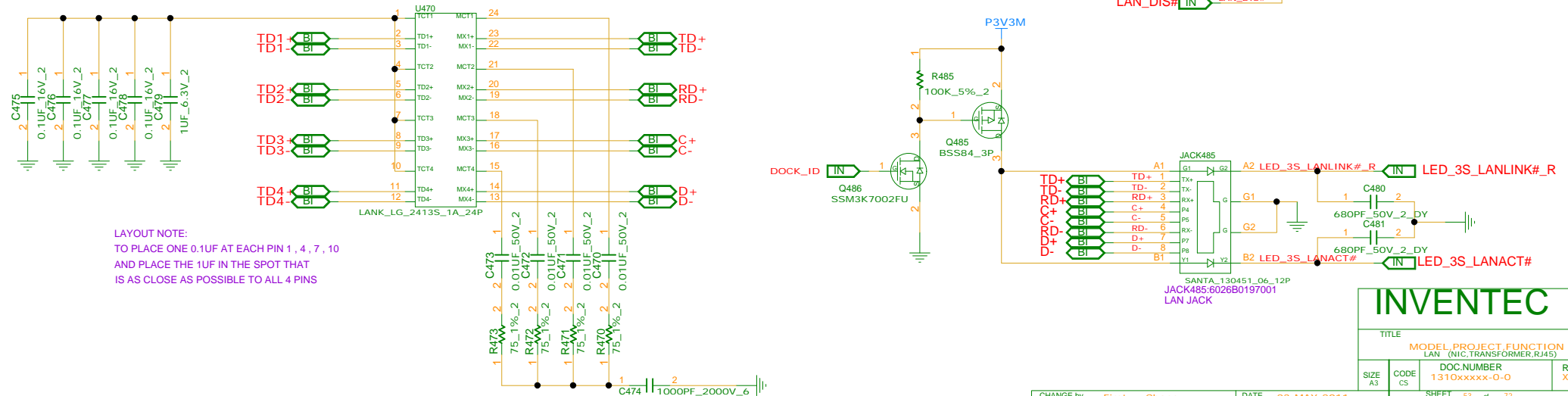
SIZE A3	CODE CS	1310xxxxx-0-0	X
SHEET 53 of 73			

1

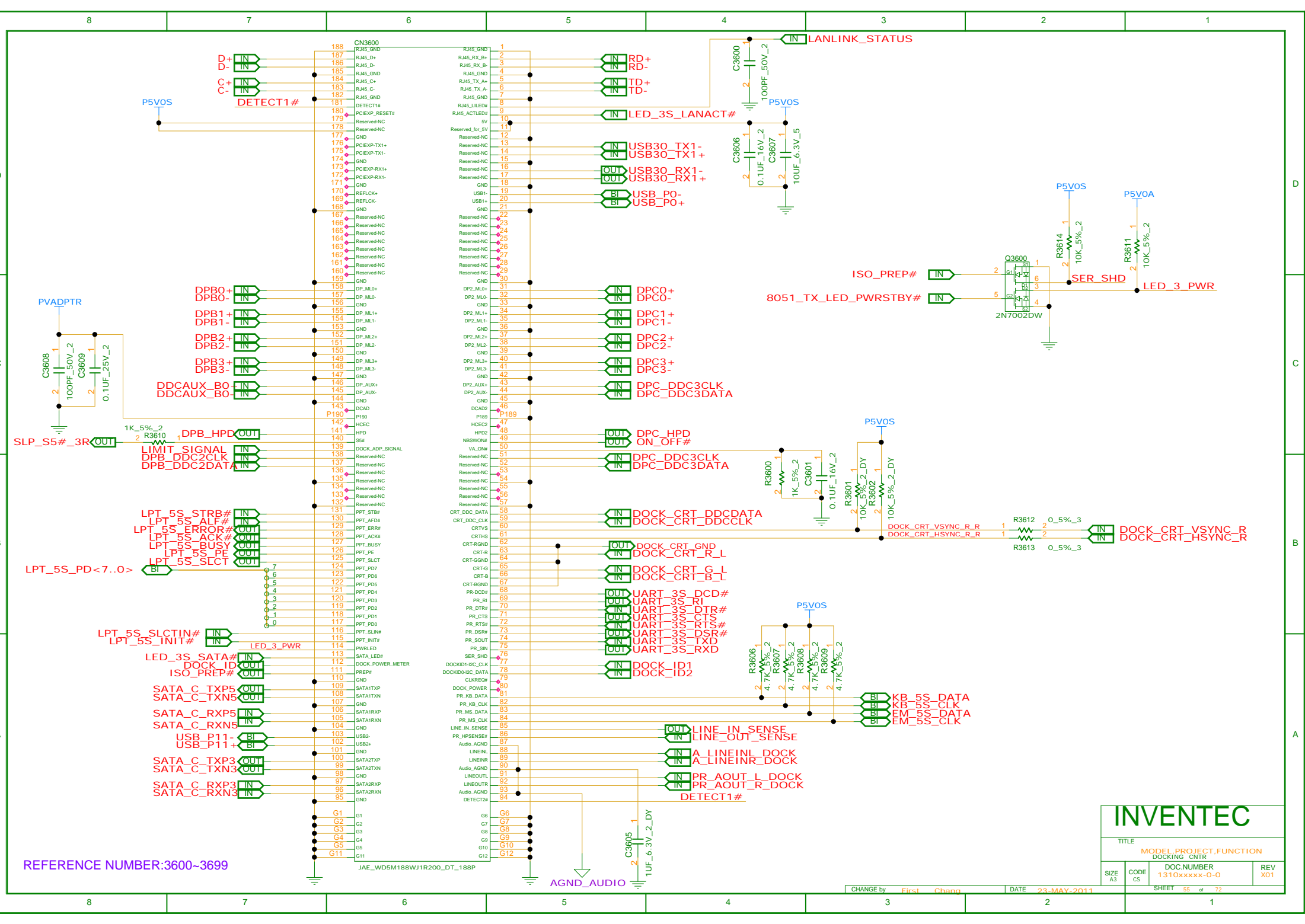
REFERENCE NUMBER : 400~469



REFERENCE NUMBER : 470~499



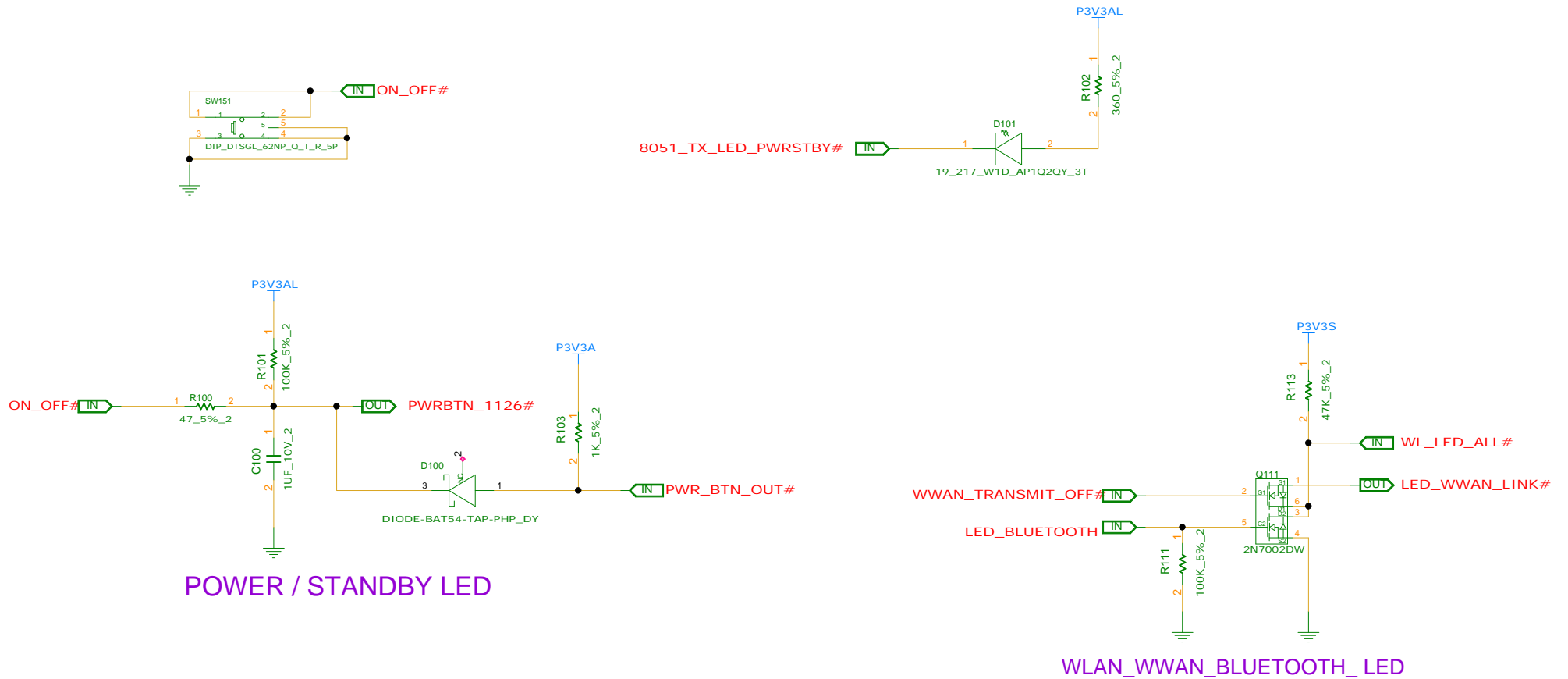
INVENTEC			
TITLE			
MODEL PROJECT.FUNCTION LAN (NIC,TRANSFORMER,RJ45)			
SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0	REV X01
SHEET 63 of 72			



REFERENCE NUMBER:3600~3699

INVENTEC

TITLE			
MODEL PROJECT FUNCTION			
DOCKING CNTR			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
SHEET 55 of 72			



REFERENCE NUMBER:100~199

INVENTEC

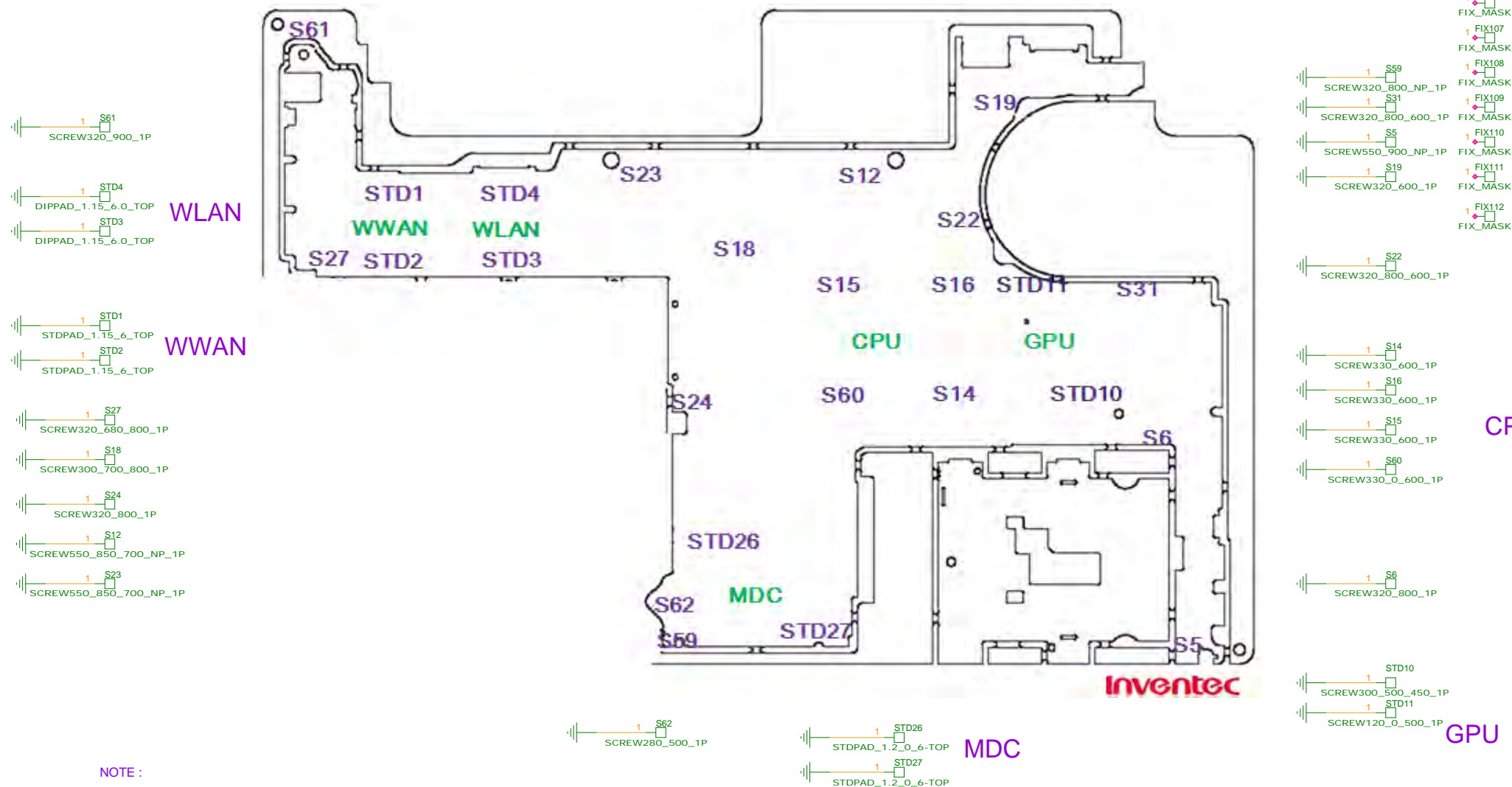
TITLE
MODEL PROJECT FUNCTION
POWER & WLAN_LED

SIZE A3 CODE CS DOC NUMBER 1310xxxxx-0-0 REV X01

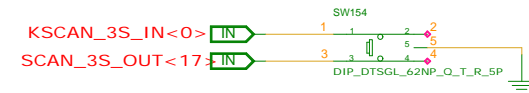
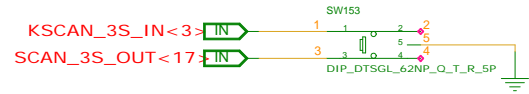
CHANGE by First Chang DATE 23-MAY-2011

SHEET 56 of 72

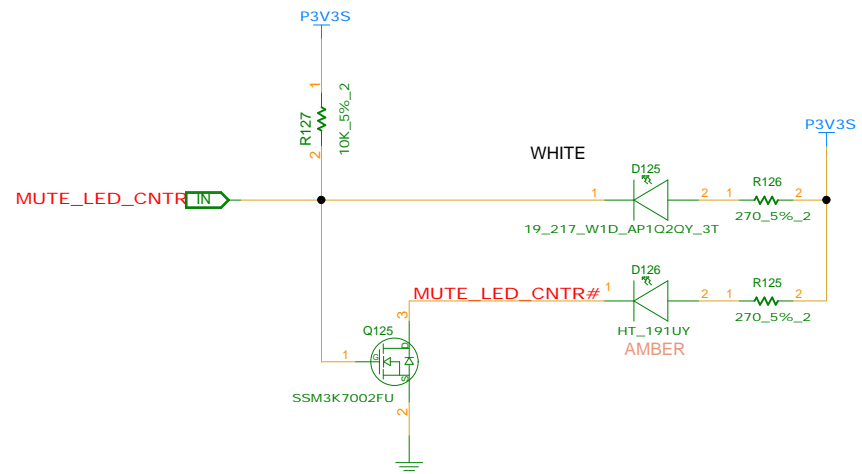
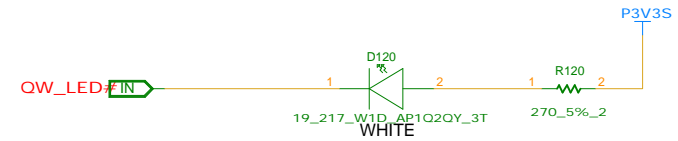
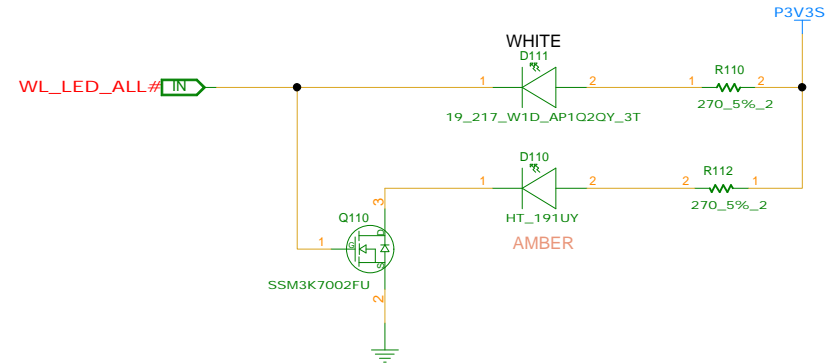
SCREW



INVENTEC			
TITLE			
MODEL PROJECT FUNCTION			
SCREW			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
CHANGE by First Chang DATE 23-MAY-2011 SHEET 57 of 72			



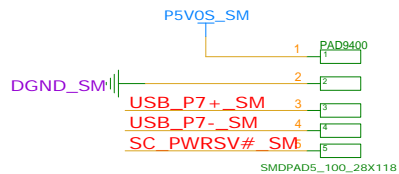
REFERENCE NUMBER:100~199



INVENTEC			
TITLE			
MODEL PROJECT FUNCTION			
WIRELESS / QUICK WEB / MUTE LED			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
SHEET 58 of 72			

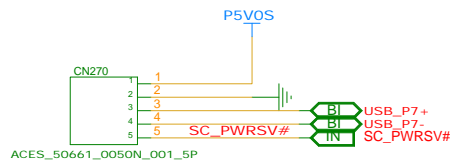
CHANGE by First Chang DATE 23-MAY-2011

FIX9401
FIX_MASK
FIX9402
FIX_MASK
FIX9403
FIX_MASK
FIX9404
FIX_MASK
FIX9405
FIX_MASK
FIX9406
FIX_MASK
FIX9407
FIX_MASK
FIX9408
FIX_MASK

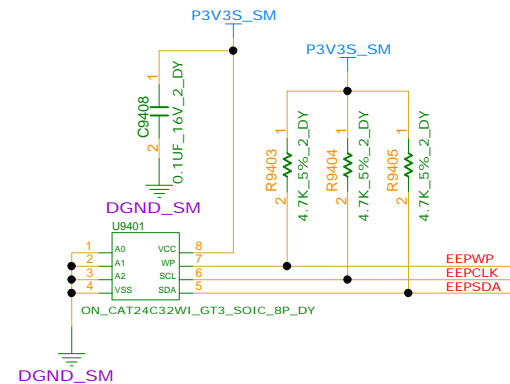
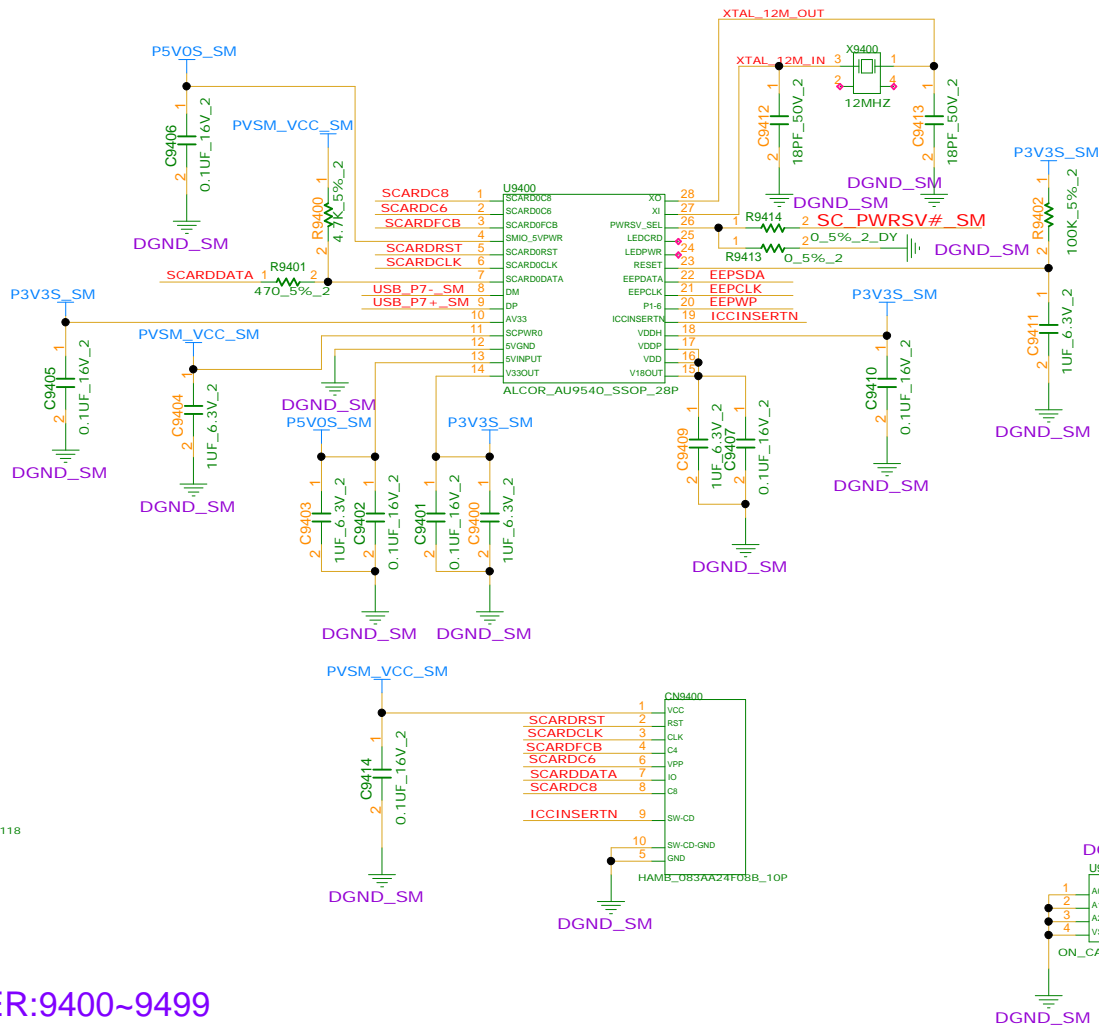


SMART CARD DB

REFERENCE NUMBER:9400~9499

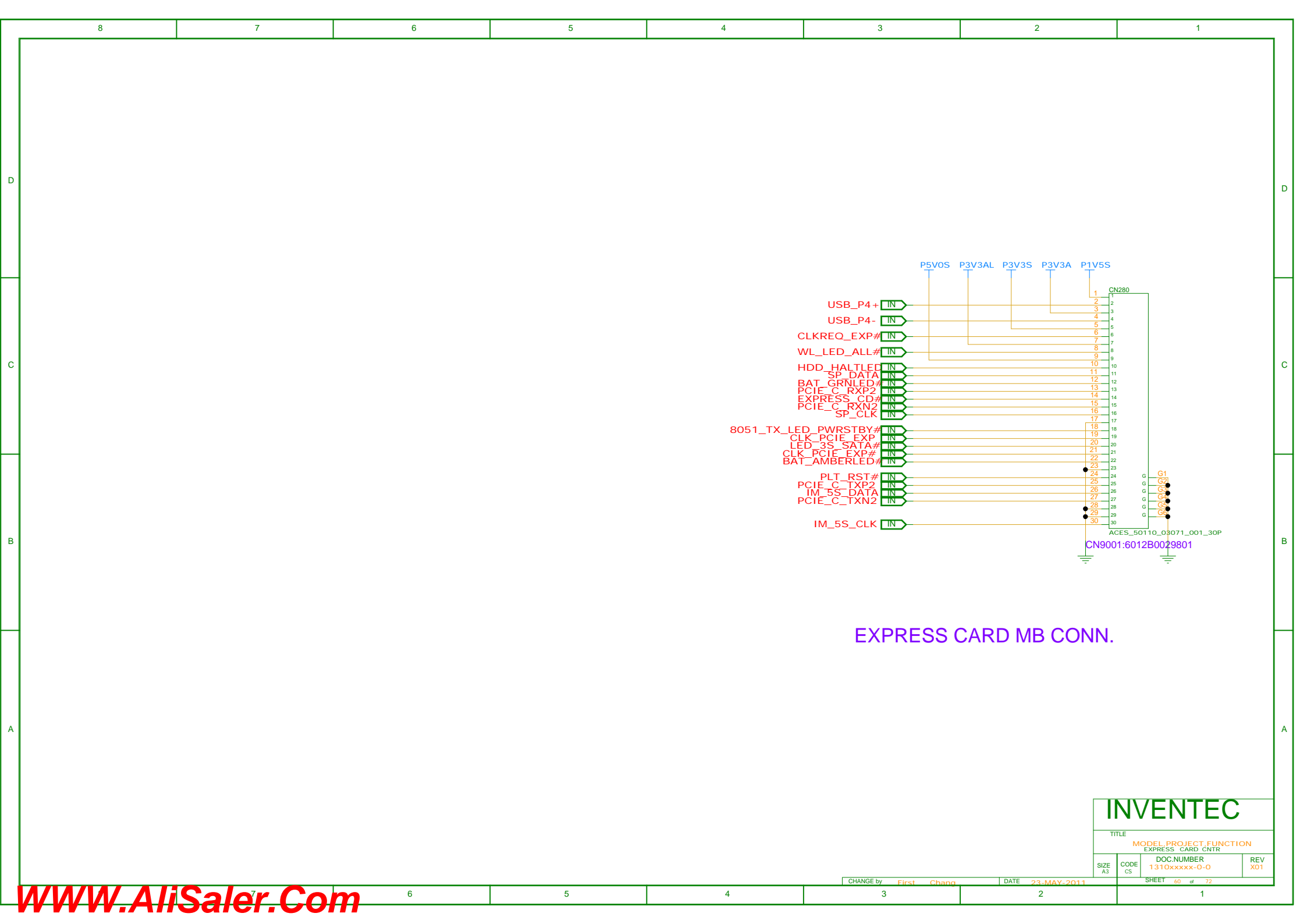


SMART CARD MB CONN.



INVENTEC

TITLE			
MODEL PROJECT,FUNCTION			
SMART CARD			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01



EXPRESS CARD MB CONN.

INVENTEC

TITLE
MODEL PROJECT.FUNCTION
EXPRESS_CARD_CNTR

SIZE CODE DOC NUMBER REV
A3 CS 1310xxxxx-0-0 X01

CHANGE by First Chang DATE 23-MAY-2011

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BLANK

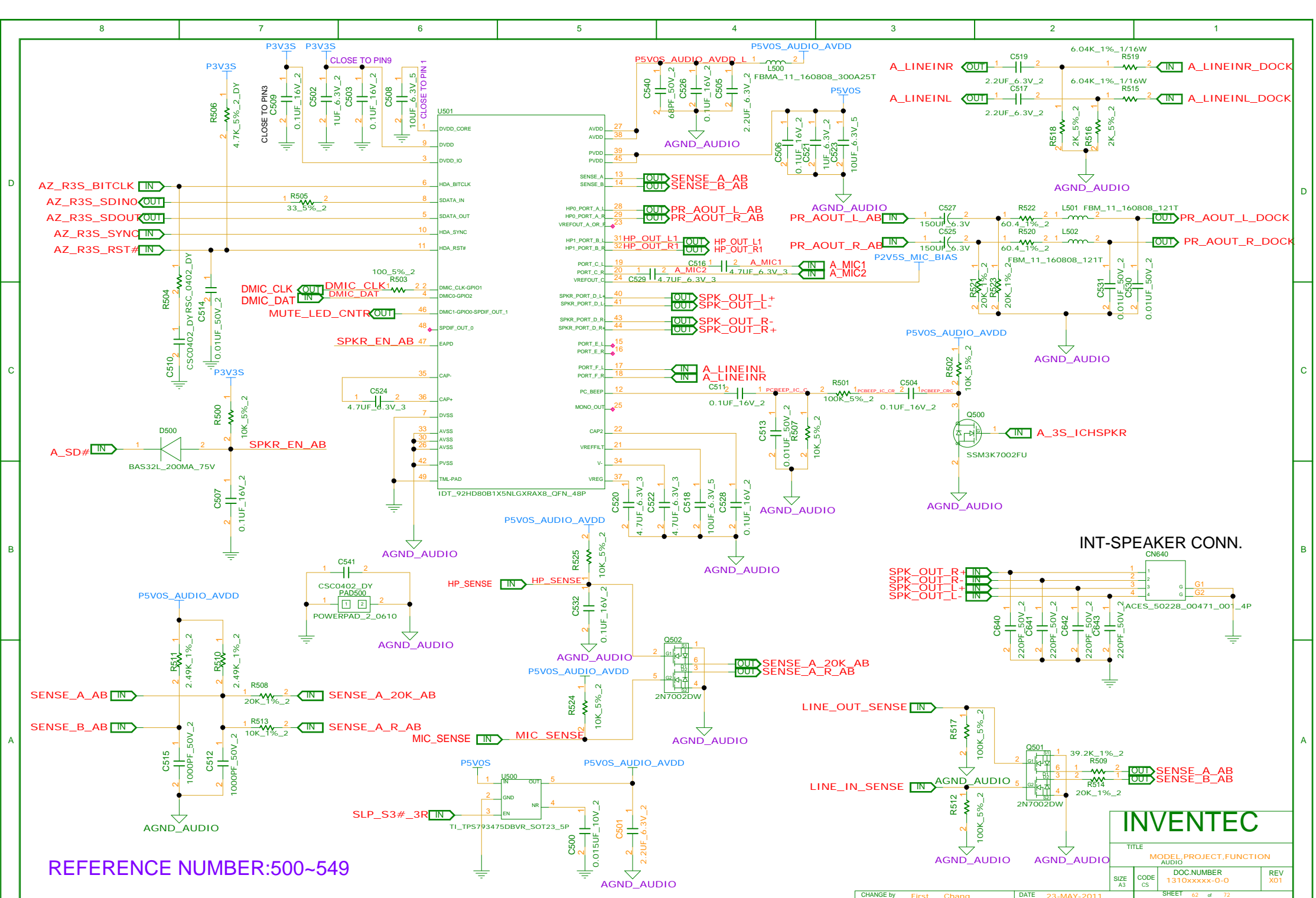
INVENTEC

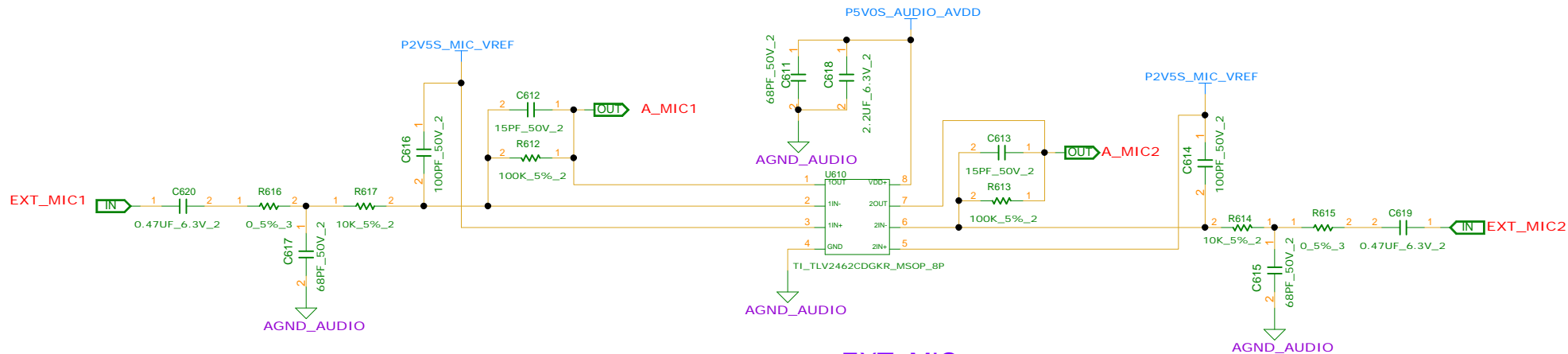
TITLE
MODEL,PROJECT,FUNCTION

SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
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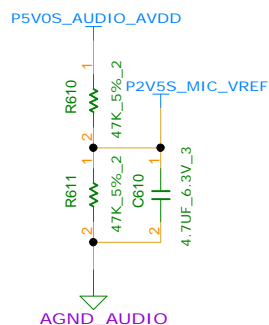
CHANGE by First Chang DATE 23-MAY-2011

SHEET 61 of 72



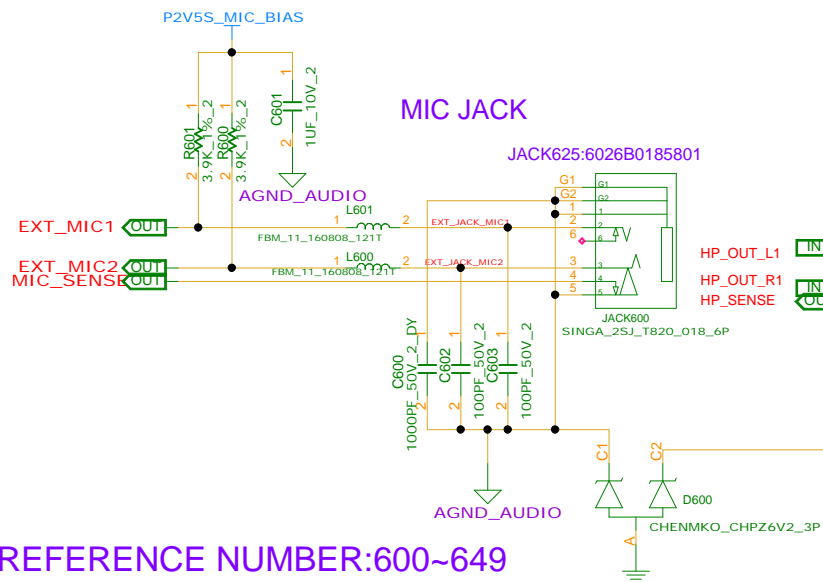


EXT. MIC
REFERENCE NUMBER:610~624

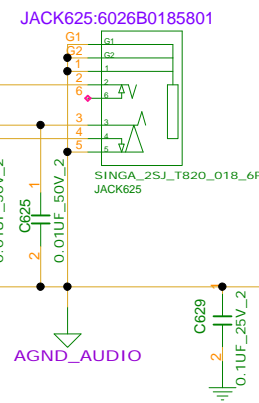


MIC JACK

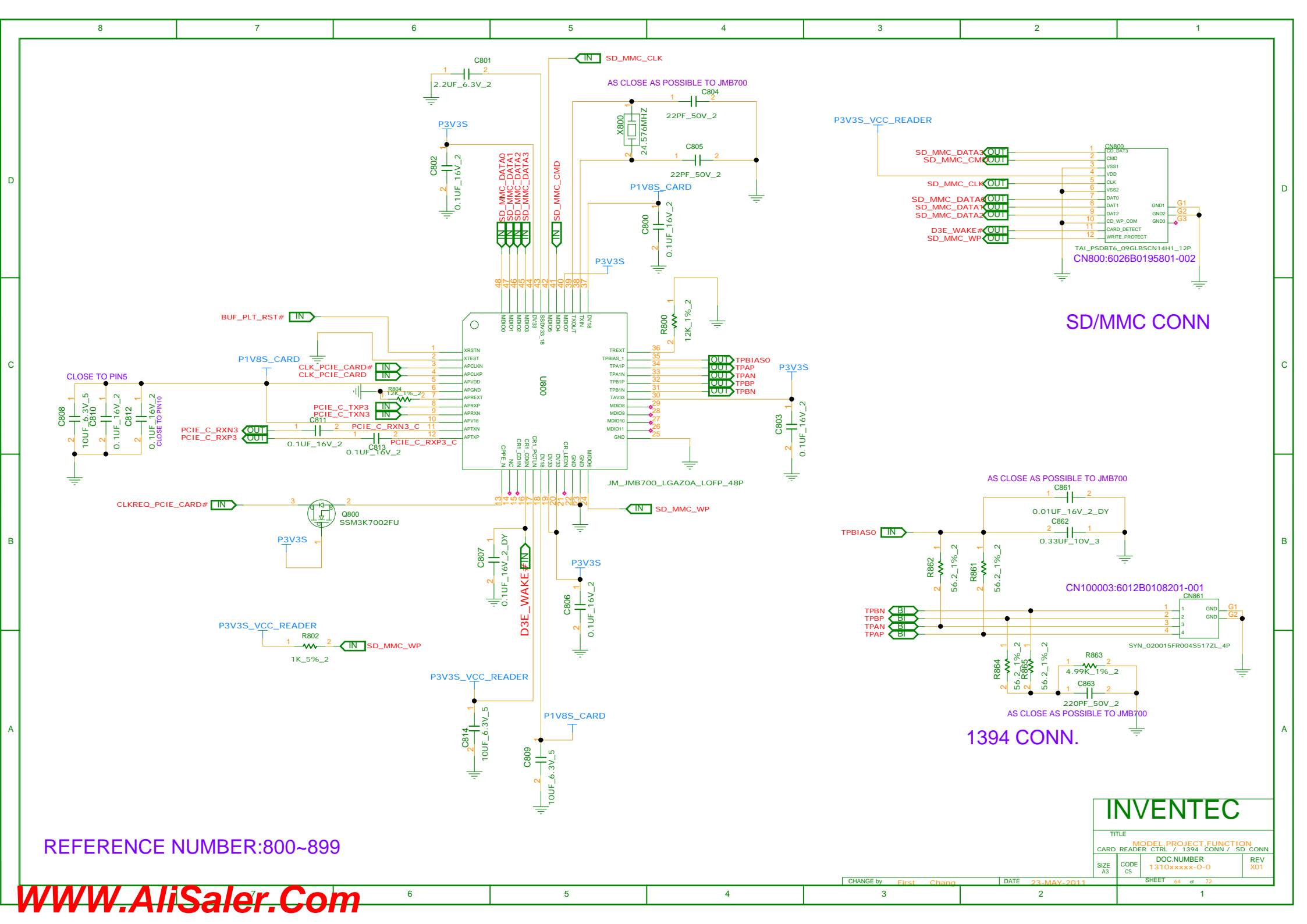
EARPHONE JACK



REFERENCE NUMBER:600~649



INVENTEC			
TITLE			
MODEL, PROJECT, FUNCTION			
EXT. MIC / MIC / HP			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
CHANGE by		DATE	SHEET
First Chang		23-MAY-2011	63 of 72

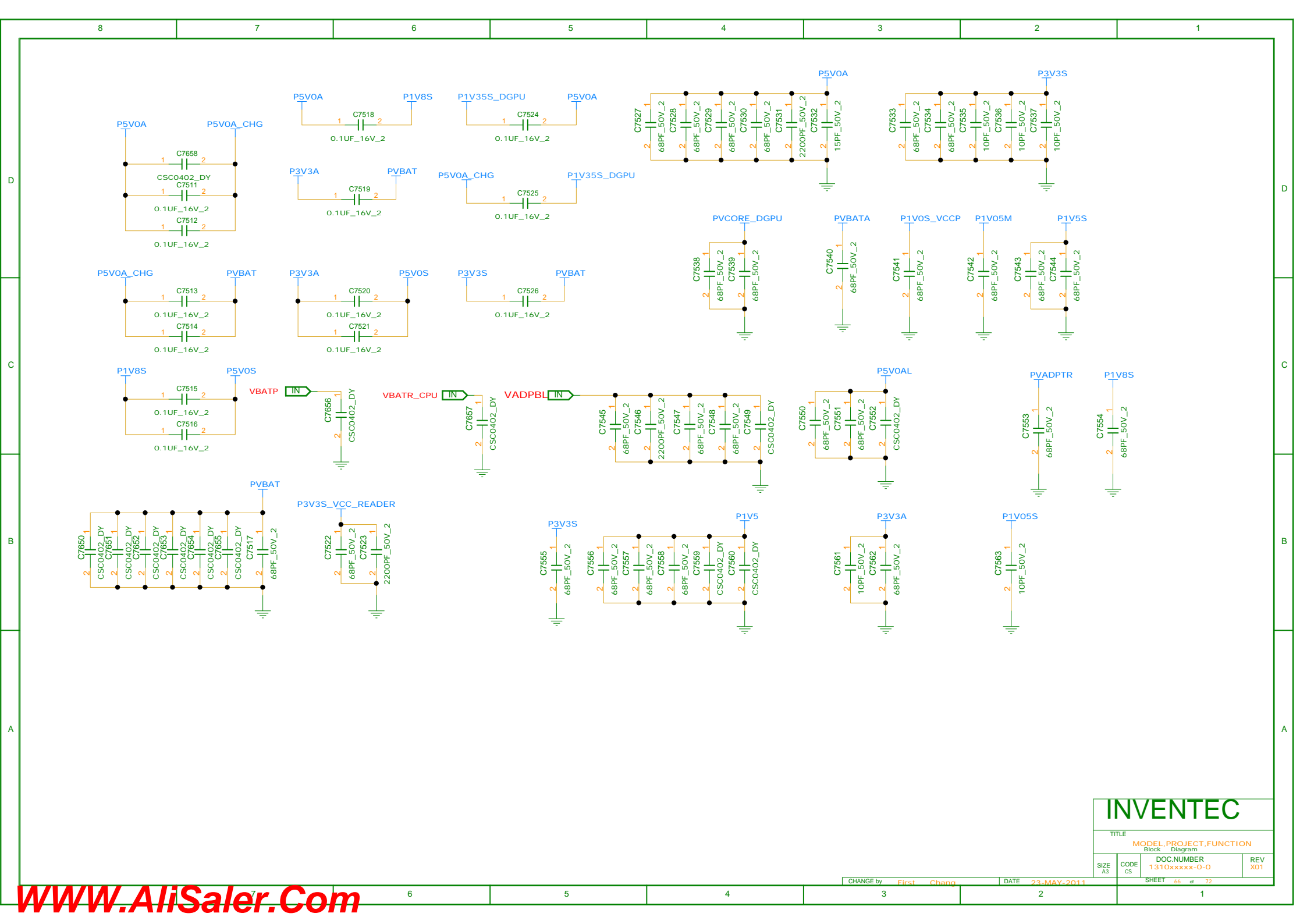


SD/MMC CONN

1394 CONN.

INVENTEC

TITLE			
MODEL PROJECT FUNCTION			
CARD READER CTRL / 1394 CONN / SD CONN			
SIZE		DOC NUMBER	
A3		1310xxxxx-0-0	
CODE		REV	
CS		X01	



INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
Block Diagram

SIZE CODE DOC NUMBER REV
A3 CS 1310xxxxx-0-0 X01

CHANGE by First Chang DATE 23-MAY-2011

SHEET 64 of 72

STRAPS	PIN	DESCRIPTION FOR DEFAULT SETTING	
TX_PWRS_ENB	GPIO0	PCIe FULL TX OUTPUT SWING.	1
TX_DEEMPH_EN	GPIO1	PCIe transmitter de-emphasis enable.	1
BIF_DEBUG_ACCESS	GPIO4	Debug signals muxed out.	0
ROMIDCFG	GPIO[11:13,9]	Memory aperture type.	XXXX

GPIO_13	GPIO_12	GPIO_11	MEMORY APERTURE SIZE
0	0	1	512MB/256MB memory aperture (Default)
1	1	0	Reserved

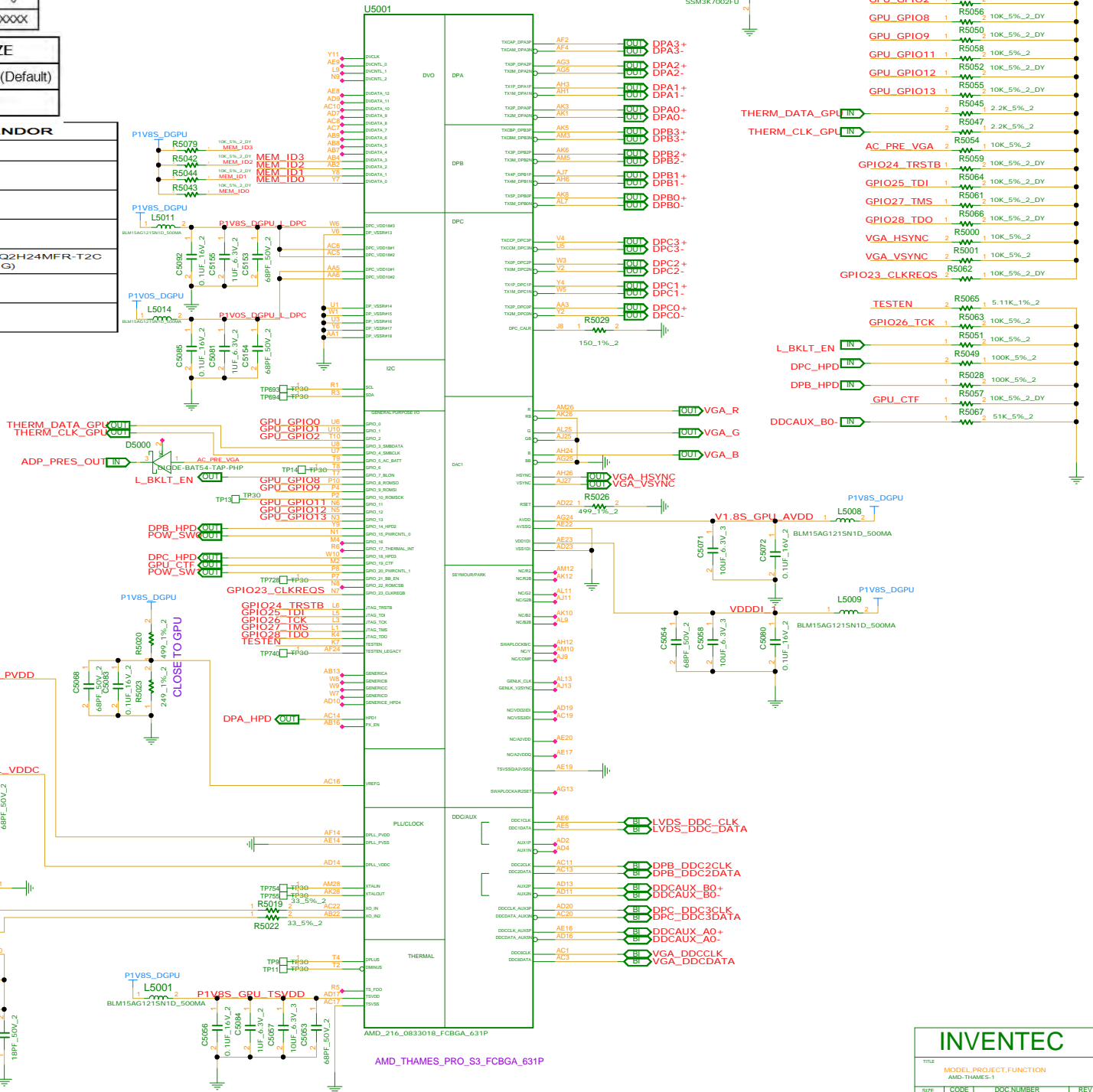
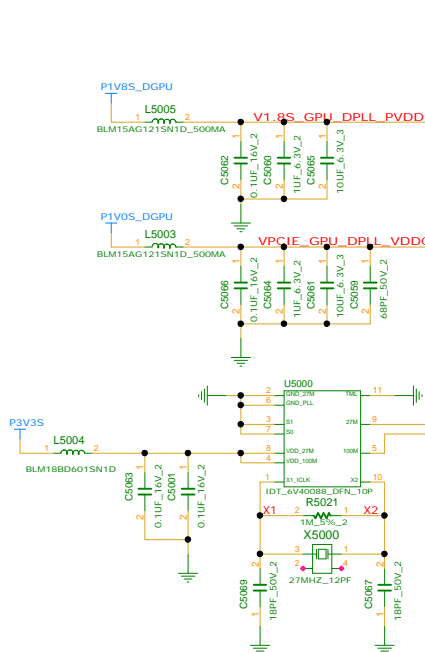
MEM_ID3	MEM_ID2	MEM_ID1	MEM_ID0	VENDOR
R5079	R5042	R5044	R5043	
0	0	0	0	
0	0	0	1	
0	0	1	0	
0	0	1	1	Hynix - H5GQ2H24MFR-T2C (128Mx16=1G)
0	1	0	0	
0	1	0	1	

POW_SW1	POW_SW0	PVCORE_DGPU
0	0	1.12V
0	1	1.0V
1	0	0.95V
1	1	0.9V

X = DESIGN DEPENDANT

0 = DO NOT INSTALL RESISTOR

1 = INSTALL 10K RESISTOR



INVENTEC			
TITLE	MODEL/PROJECT/FUNCTION	AMD-THAMES-S1	
SIZE	CODE	DOCNUMBER	REV
E	CS	13110XXXX-0-0	201
SHEET		of	2

CHANGE by: *Cesay* *Chang* DATE: 23 MAY 2011

